

LTspice Model

CRD

SEMITEC

E-18326R



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_E-18326R_LT
Pin Assign 1:A 2:C
File List Model Library MDC_E-18326R_LT01.lib
 Model Report MDC_E-18326R_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2014/06
- Product name E-18326R
- Company name SEMITEC Corporation
- Characteristics IfVf, IfTemp

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-------------|-------|----|------|-------|
| | Min. | | Max. | |
| Temperature | -30 | to | 150 | deg C |

Diode

○ : Implemented
× : Not Implemented
— : Not applicable

Model Functions Table

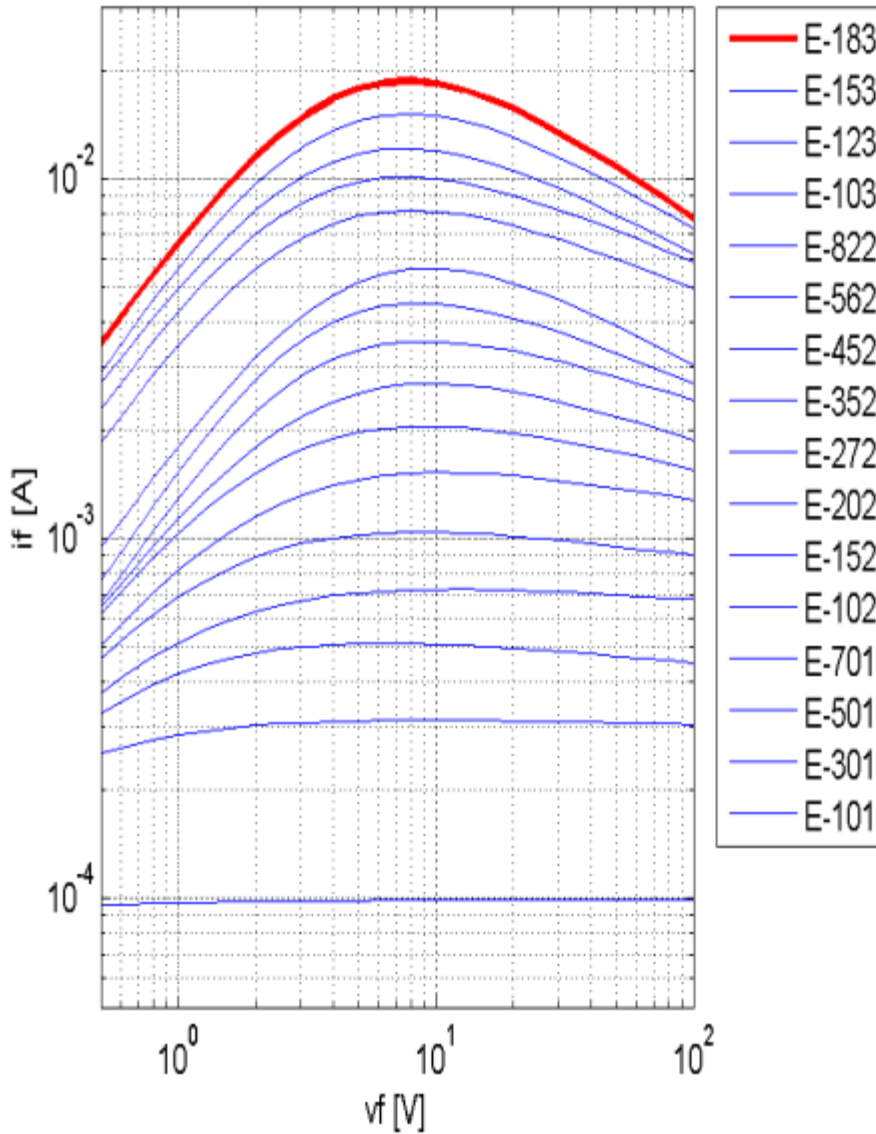
RANK=1

| Functions | RANK | Implemented |
|---------------------------------------|------|-------------|
| IF-VF(Temp) | 1 | ○ |
| IR-VR(Temp) | 1 | — |
| Capacitance | 1 | — |
| Reverse recovery characteristics | 1 | — |
| Zz-Iz | 1 | — |
| Rectification characteristics(Bridge) | 1 | — |
| Surge voltage-Transient | 1 | — |

Simulation results are following.
Explanatory notes — : simulated

IfVf

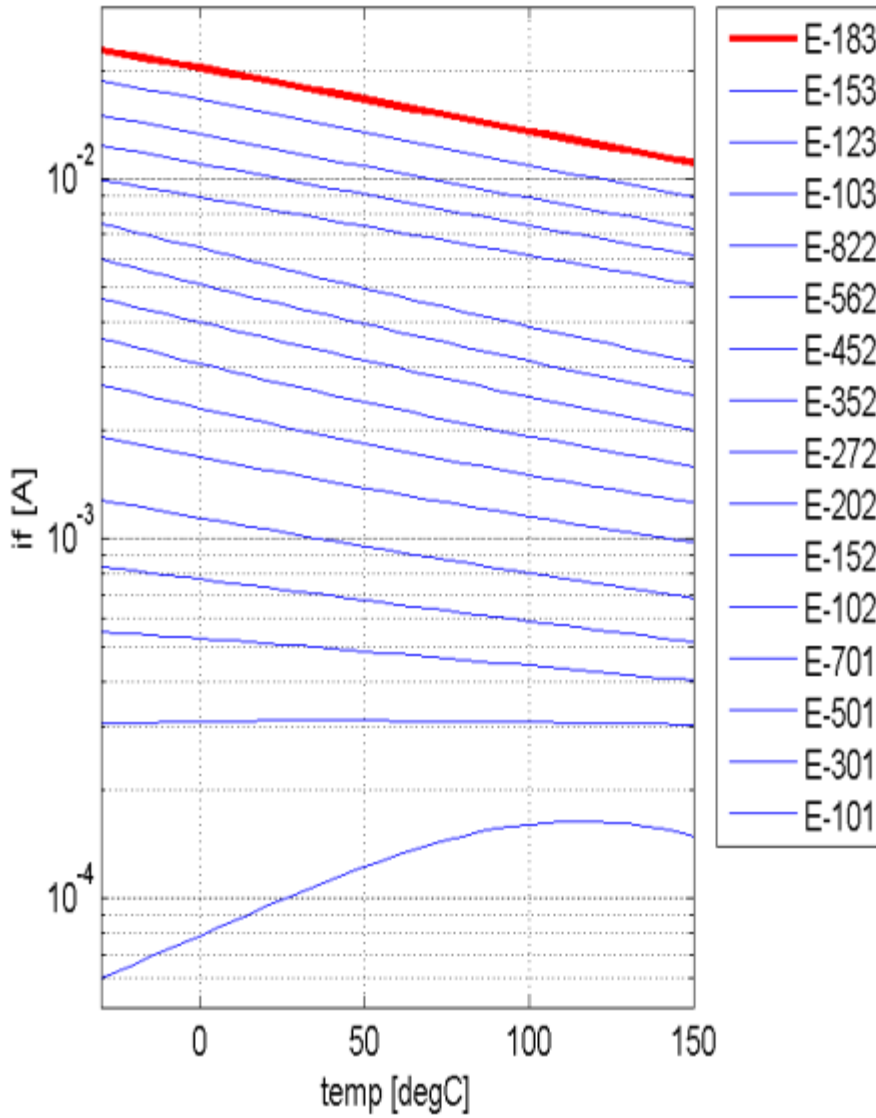
Temp = 25degC



Simulation results are following.
Explanatory notes — : simulated

IfTemp

Vf = 10V



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