

# PSpice Model

## NJFET

## NXP

## BF861C

### Model Information

**Model** JFET model  
**Call Name** MDC\_BF861C\_PS  
**Pin Assign** 1:S 2:D 3:G  
**File List** Model Library MDC\_BF861C\_PS01.lib  
 Model Report MDC\_BF861C\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 17.2  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2011/09/15
- Product name BF861C
- Company name NXP
- Characteristics IdVgs, IdVdsVgs, IDSS, Crss, Ciss

### Simulation Range

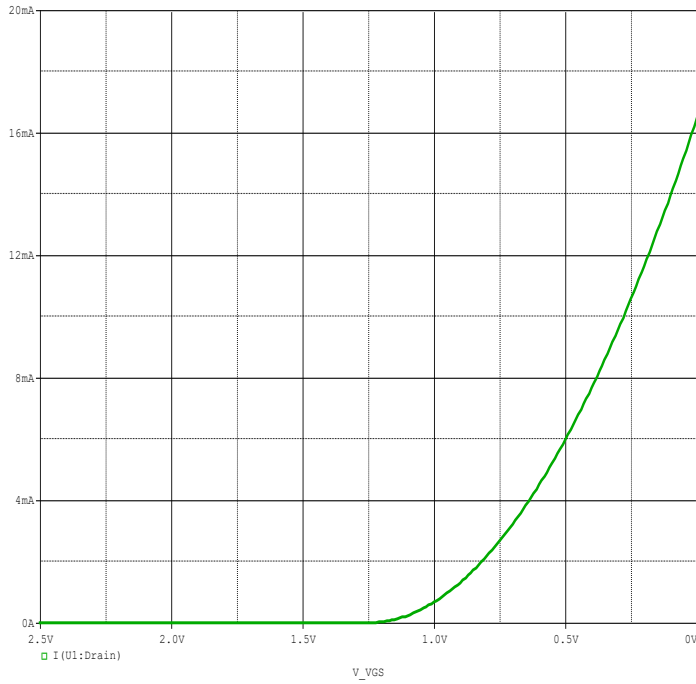
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-Source voltage (DC)	0	to	25	V
Forward gate current (DC)	0	to	10m	A
Temperature	-65	to	150	deg C

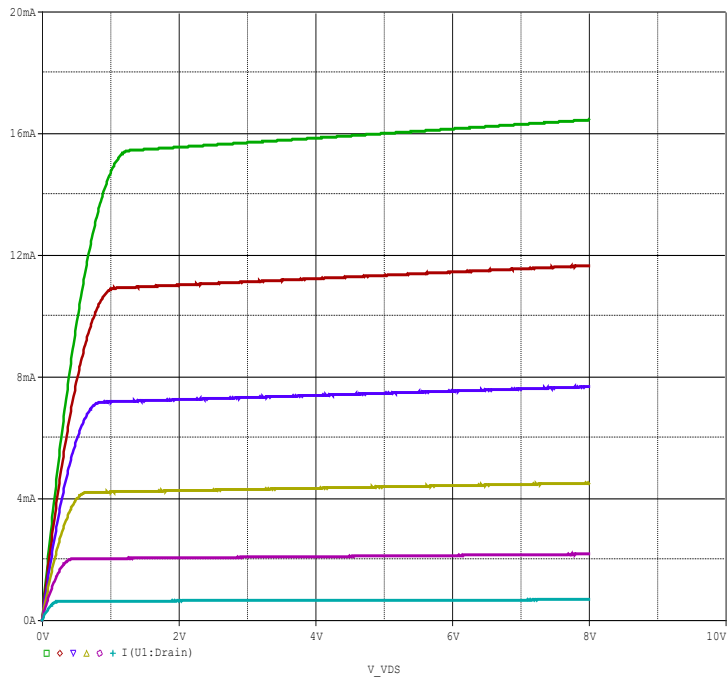
Simulation results are following.  
 Explanatory notes — : simulated

**IdVgs**

Vds = 8V



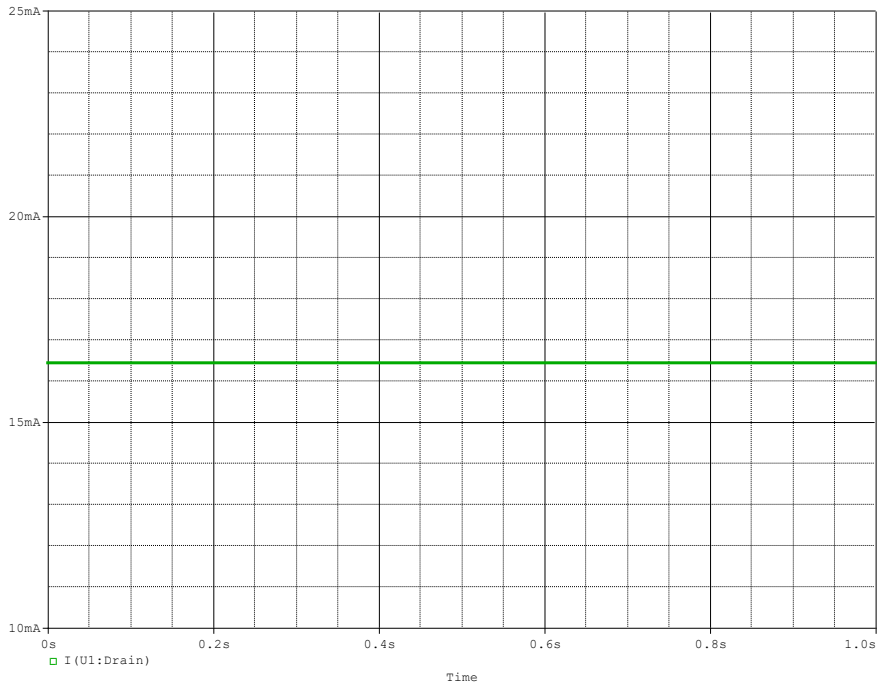
**IdVds**



Simulation results are following.  
 Explanatory notes — : simulated

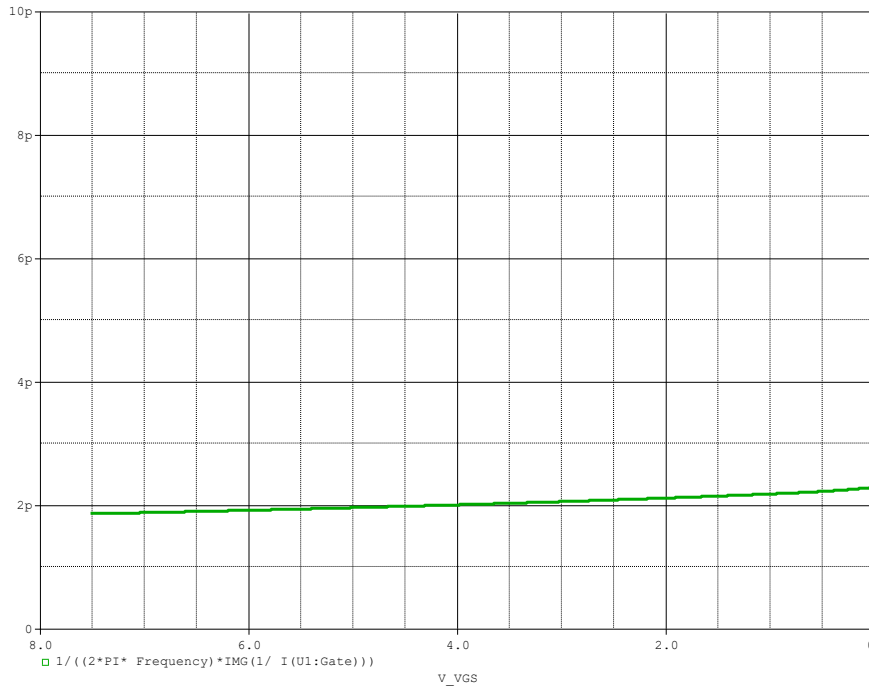
**IDSS**

V<sub>ds</sub> = 8V



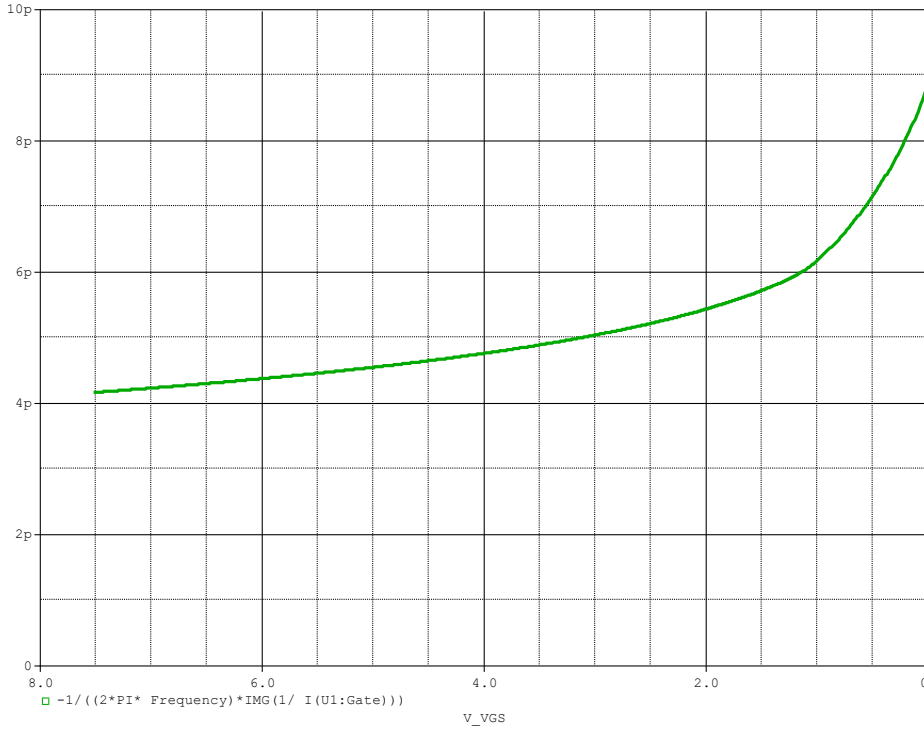
**Crss**

V<sub>ds</sub> = 8V



Simulation results are following.  
 Explanatory notes — : simulated

**Ciss**  
 Vds = 8V



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