

LTspice Model

N-Channel JFET

Isahaya Electronics

2SK492-T150

Model Information

Model Berkeley SPICE JFET Model
Call Name MDC_2SK492-T150_LT
Pin Assign 1:D 2:G 3:S
File List Model Library MDC_2SK492-T150_LT01.lib
 Model Report MDC_2SK492-T150_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2017/03
- Product name 2SK492-T150
- Company name Isahaya Electronics Corporation
- Characteristics IDVGSTEMP, IDVDSVGS, IDVDSVGS02, IDVDSVGS03
RDSOINIDTEMP, YFSIDTEMP

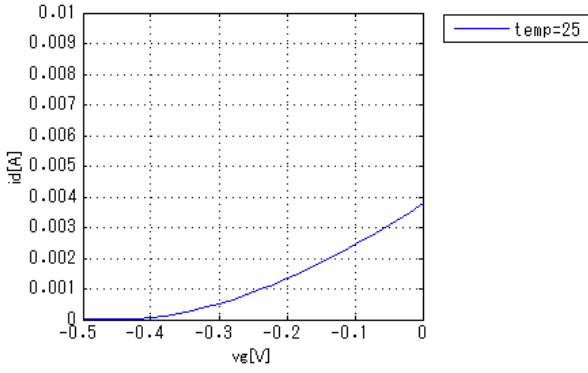
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

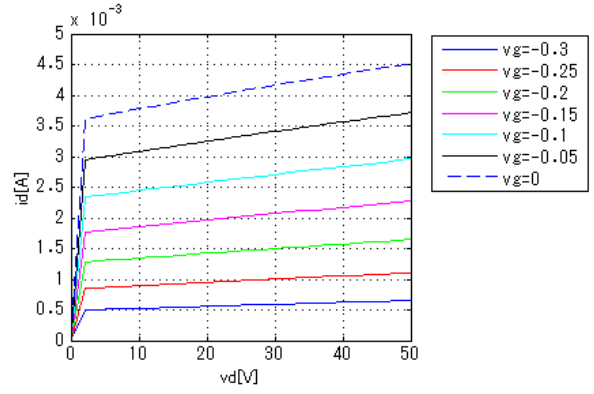
Item	Range			Unit
	Min.		Max.	
Gate-Drain voltage (DC)	0	to	-50	V
Gate current (DC)	0	to	10	mA
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

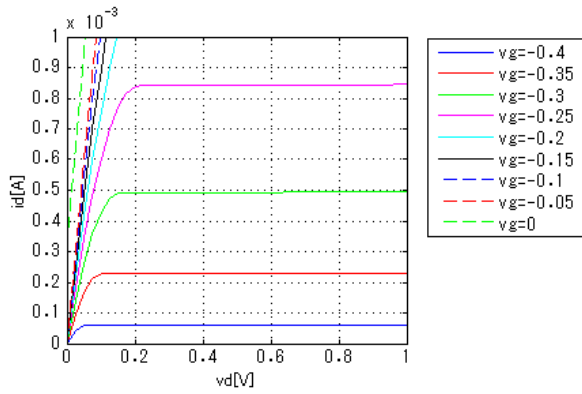
IDVGSTEMP



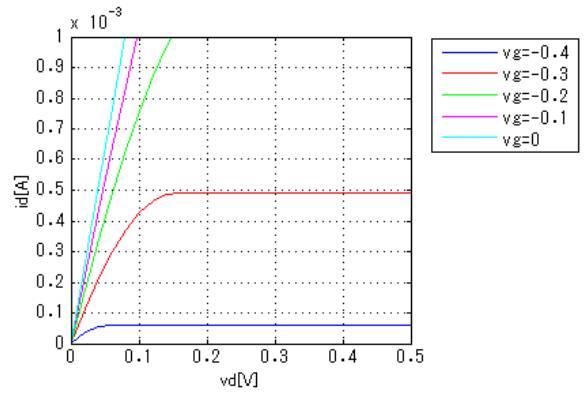
IDVDSVGS



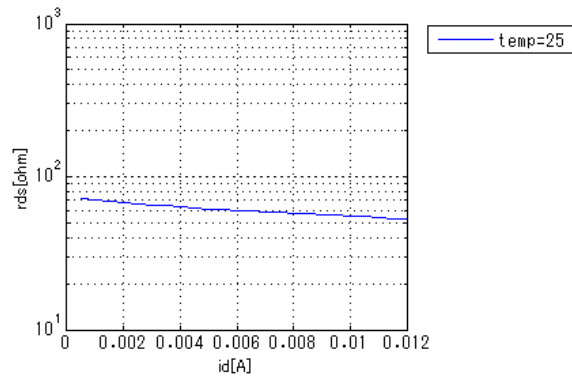
IDVDSVGS02



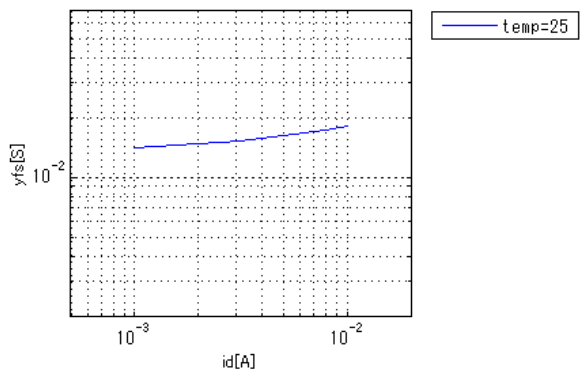
IDVDSVGS03



RDSonIDTEMP



YFSIDTEMP



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