

# LTspice Model OpAmp Texas Instruments OPA2322AIDR

## **Model Information**

Model A macro model

Call Name MDC\_OPA2322AIDR\_LT

**Pin Assign** 1:OUTA,2:-INA,3:+INA,4:V-,5:+INB,6:-INB,7:OUTB,8:V+

File List Model Library MDC\_OPA2322AIDR\_LT.lib

Model Report MDC\_OPA2322AIDR\_LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version SBOS538F –JANUARY 2011–REVISED DECEMBER 2016

Product name
OPAx322x

Company name
Texas Instruments Incorporated.

[Characteristics listed]

Characteristics

Open Loop Gain, Phase Input Offset Voltage

Bias Current

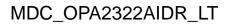
Maximum output amplitude voltage

Slew Rate

#### **Simulation Condition**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C





O : Implemented × : Not Implemented

—: Not applicable

**Model Functions Table** 

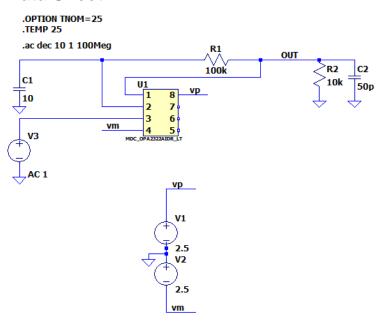
RANK=1

	NAINK-1	
Functions	RANK	Implemented
Open Loop Gain	1	0
Unity Frequency	1	0
Phase Margin	1	0
Input Offset Voltage	1	0
Input Offset Current	1	0
Bias Current	1	0
Maximum output amplitude voltage	1	0
Slew Rate	1	0
Equivalent Input Noise Voltage	2	×
Equivalent Input Noise Current	2	×



## Open Loop Gain , Phase Testbench

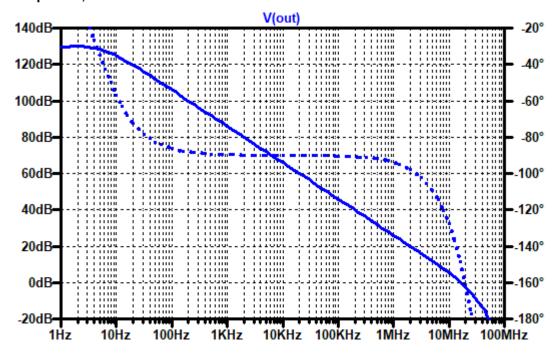
## **Referred to Data Sheet**



Simulation results are following.

Explanatory notes — : simulated

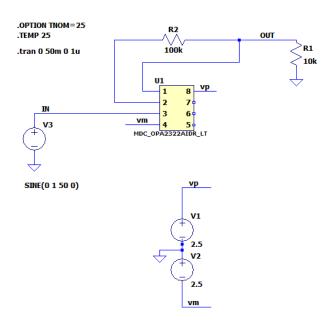
#### Open Loop Gain, Phase





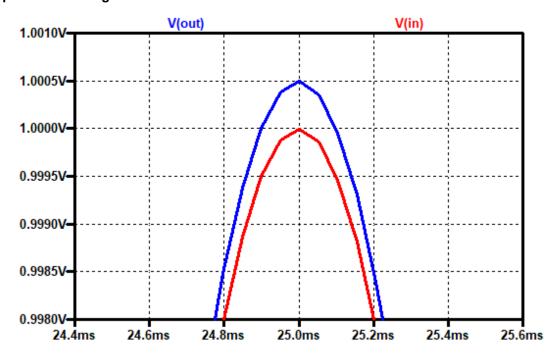
# **Input Offset Voltage Testbench**

# **Referred to Data Sheet**



Simulation results are following. Explanatory notes — : simulated

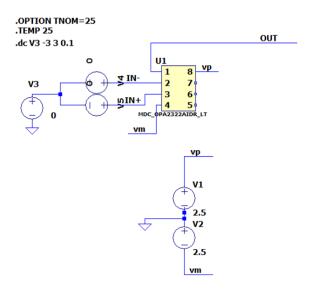
## **Input Offset Voltage**





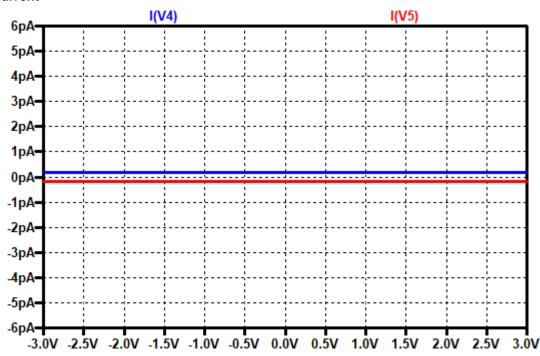
#### **Bias Current Testbench**

# **Referred to Data Sheet**



Simulation results are following. Explanatory notes — : simulated

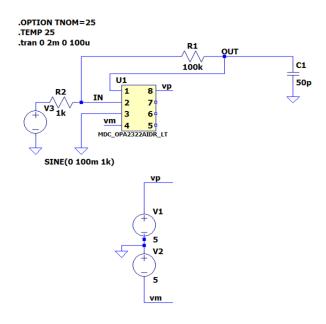
#### **Bias Current**





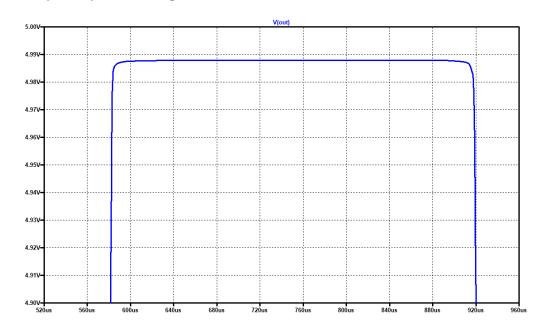
# Maximum output amplitude voltage Testbench

# **Referred to Data Sheet**



Simulation results are following. Explanatory notes — : simulated

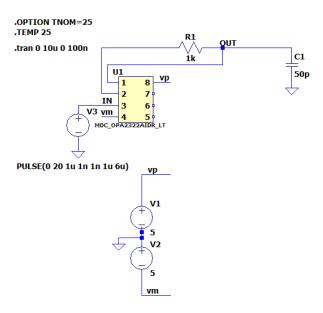
## Maximum output amplitude voltage





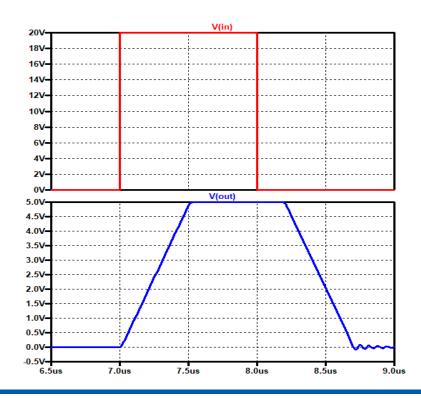
#### **Slew Rate Testbench**

# **Referred to Data Sheet**



Simulation results are following. Explanatory notes — : simulated

#### **Slew Rate**





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