

LTspice Model Power Clamper SHINDENGEN ST03D-170



Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_ST03D-170_LT

 Pin Assign
 1:① 2:②

 File List
 Model Library
 MDC_ST03D-170_LT03.lib

 Model Report
 MDC_ST03D-170_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]
Date/Version
Product name
Company name
Characteristics

Rev.01(2020.01) ST03D-170 Shindengen Electric Manufacturing Co., Ltd. BvTemp,CjFreq[Vf],CjFreq[Vr],CjFreq[Vf],CjVr,CjVf,IrVr[Tem p],TrrIrIf,TrrWaveform,SurgeWaveform

Simulation Range

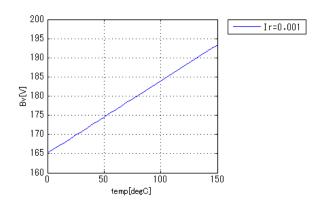
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	145	V
Forward Voltage	0	to	600	V
Temperature	-40	to	150	deg C

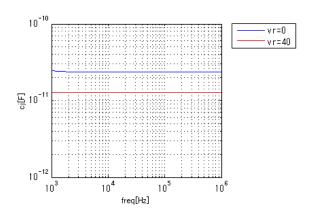


Simulation results are following. Explanatory notes -: simulated

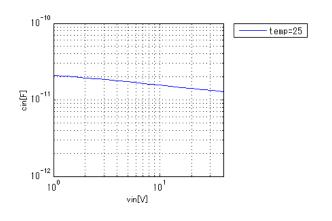
BvTemp



CjFreq[Vr]

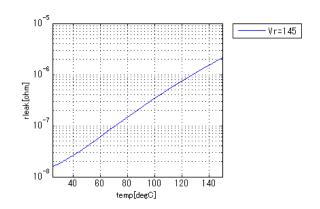






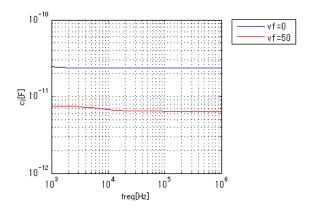
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CjFreq[Vf]

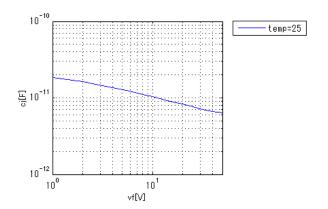




Temp = 25degC



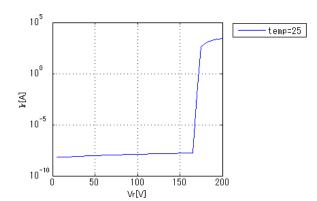
CjVf Freq. = 0.1MHz



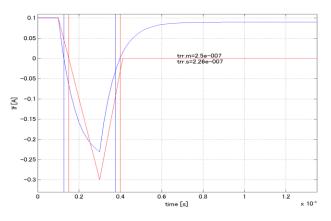


Simulation results are following. Explanatory notes -: simulated

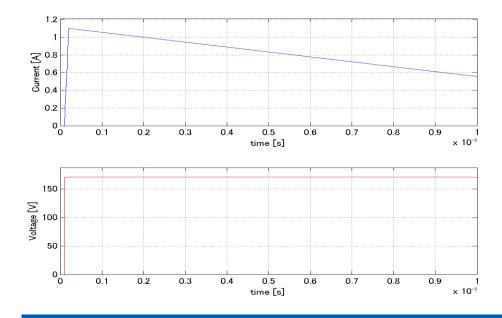
IrVr[Temp]



Trr Waveform

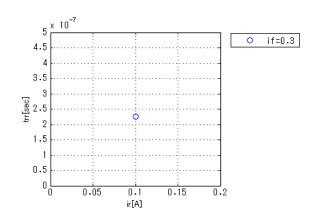


Surge Waveform (10us/1000us)



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Trririf





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MoDeCH Inc.

Head Office Location: Taiju-Seimei-Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/