PSpice Model
Zener Diode
VISHAY INTERTECH
VLIN2626-02G

Model Information

Model	A macro model based on general SPICE diode model	
Call Name	MDC_VLIN2626-02G_PS	
Pin Assign	1:C 2:C	
File List	Model Library	MDC_VLIN2626-02G_PS01.lib
	Model Report	MDC_VLIN2626-02G_PS.pdf (this file)
Verified Simulator Version		PSpice version 17.2
Note		

References

The information which was used for modeling is as follow:

[Data Sheet]

- ●Date/Version: Rev. 1.5, 19-Nov-2021
- ●Product name: VLIN2626-02G
- ●Company name: Vishay Intertechnology, Inc.
- ●Characteristics: CjVr,VrIr[Temp],VrIr[Temp]2,IrVr[Temp],SurgeReverseCurrentWaveform(1A),SurgeReverseCurrentWaveform(4A)

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Zener Voltage	28(at 1mA)	to	32(at 1mA)	V
Clamping Voltage(max)	39(at 4A)	to	39(at 4A)	V
Temperature	-55	to	150	deg C

Diode

○ : Implemented
× : Not Implemented
— : Not applicable

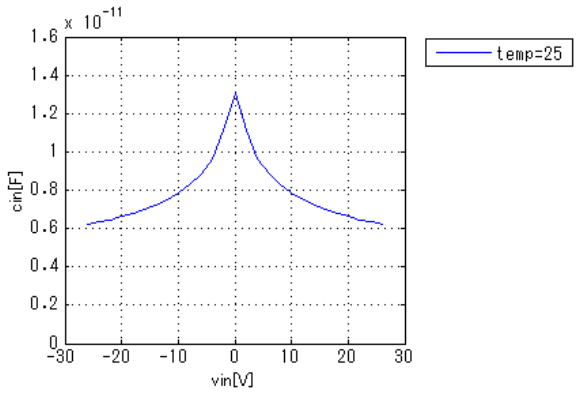
Model Functions Table

	RANK=1	
Functions	RANK	Implemented
IF-VF(Temp)	1	○
IR-VR(Temp)	1	○
Capacitance	1	○
Reverse recovery characteristics	1	—
Rectification characteristics(Bridge)	1	—

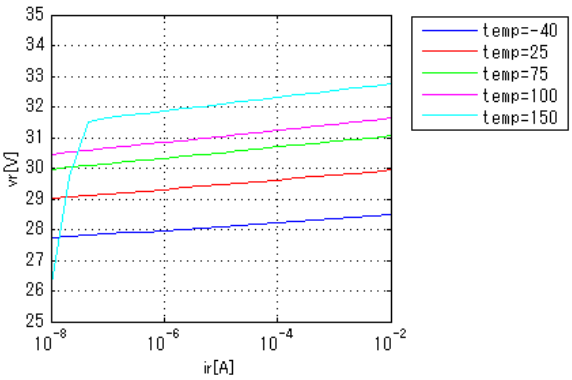
Simulation results are following.
Explanatory notes — : simulated

CjVr

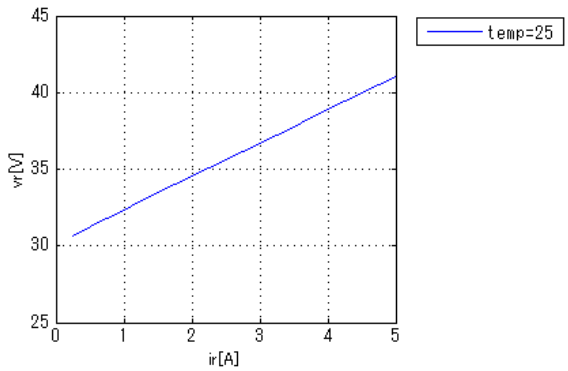
Freq = 1000000Hz



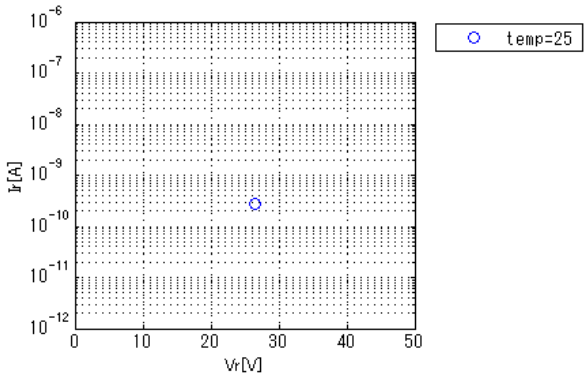
VrIr[Temp] (Zener Voltage)



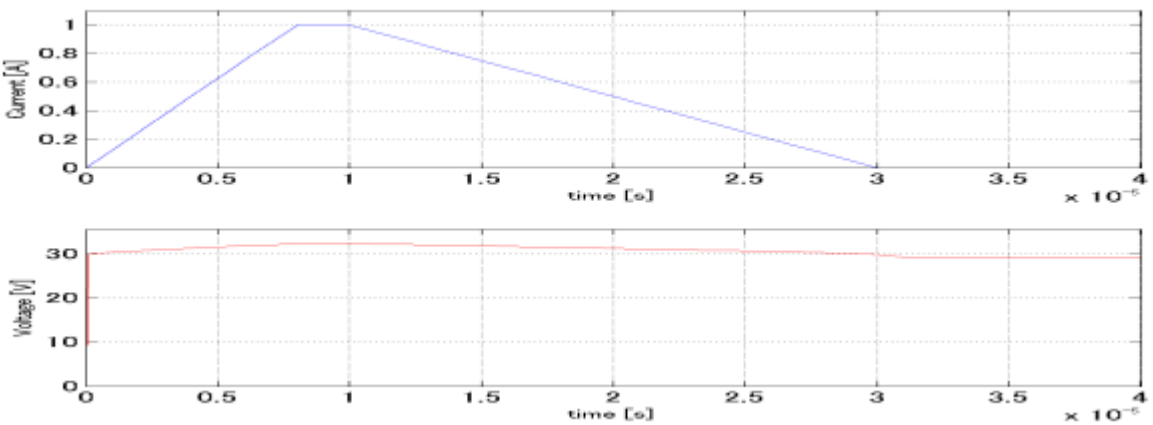
VrIr[Temp]2 (Clamping Voltage)



IrVr[Temp] (Leak Current)

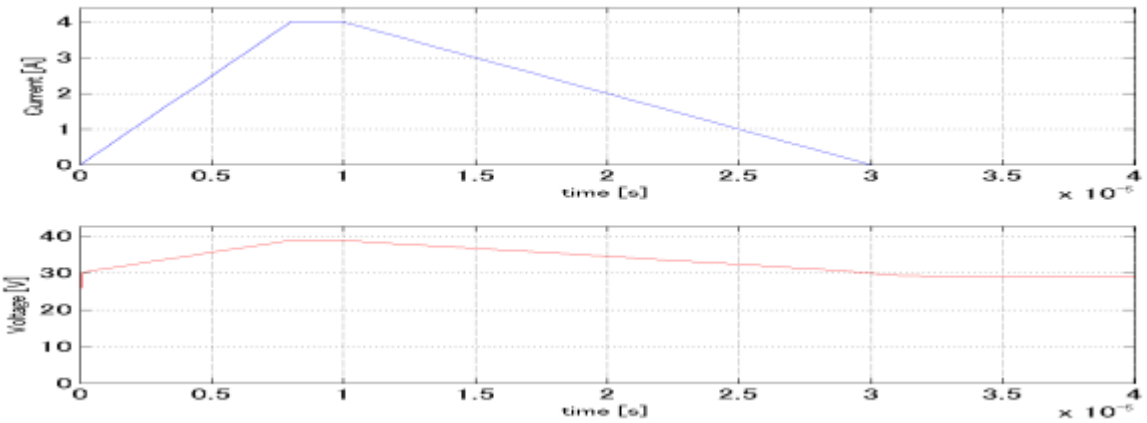


Surge Reverse Current Waveform (1A)



Simulation results are following.
Explanatory notes — : simulated

Surge Reverse Current Waveform (4A)



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