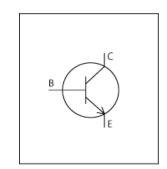


# LTspice Model NPN Nexperia PBSS4160XF



# **Model Information**

Model Gummel-Poon model
Call Name MDC\_PBSS4160XF\_LT

Pin Assign 1:E 2:C 3:B File List Model Library

Model Library MDC\_PBSS4160XF\_LT01.lib
Model Report MDC\_PBSS4160XF\_LT.pdf (this file)

**Verified Simulator Version** 

Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameCompany name23 May 2017PBSS4160XFNexperia B.V.

● Characteristics IcVce[ib],hFEIc[Temp],lcVbe[Temp],Vce(sat)lc[Temp],

Vbe(sat)lc[Temp],Cob,fTlc[Vce]

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

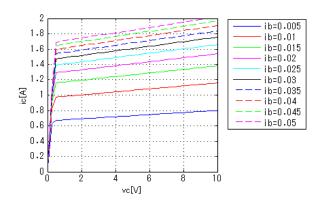
Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	60	V
Base current (DC)	0	to	0.3	Α
Temperature	-65	to	150	deg C



Simulation results are following. Explanatory notes — : simulated

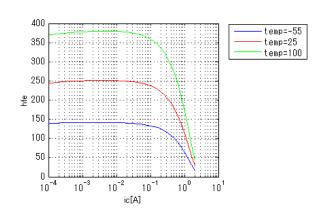
## IcVce[ib]

Temp. = 25deg C



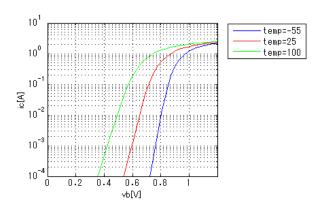
## hFEIc[Temp]

Vce = 5V



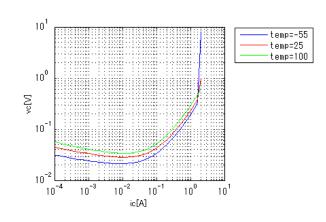
## IcVbe[Temp]

Vce = 5V



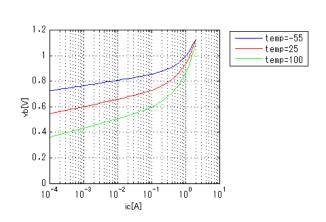
## Vce(sat)lc[Temp]

IC/IB = 20



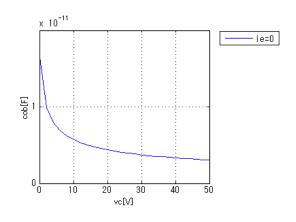
## Vbe(sat)lc[Temp]

IC/IB = 20



### Cob

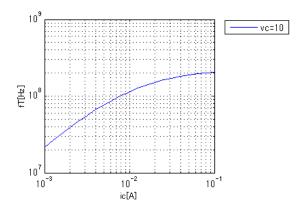
Freq. = 1MHz





Simulation results are following. Explanatory notes — : simulated

# fTIc[Vce]





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