

LTspice Model Capacitor Murata GRM033R61E472KA12D

Model Information

Model A macro model

Call Name MDC GRM033R61E472KA12D LT

Pin Assign 1:1 2:2

File List Model Library MDC GRM033R61E472KA12D LT.lib

Model Report MDC GRM033R61E472KA12D LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version

Product name GRM033R61E472KA12D

Company name Murata Manufacturing Co., Ltd.

[Characteristics listed]

Characteristics

Capacitance, Impedance-Frequency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

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MDC_GRM033R61E472KA12D_LT

O:Implemented

×: Not Implemented

—: Not applicable **Model Functions Table** RANK=1

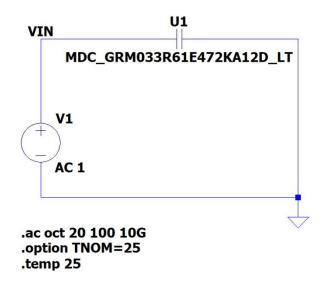
Functions	RANK	Implemented
Impedance-Frequency	1	0
Capacitance-Frequency	1	0

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Capacitance, Impedance-Frequency Testbench

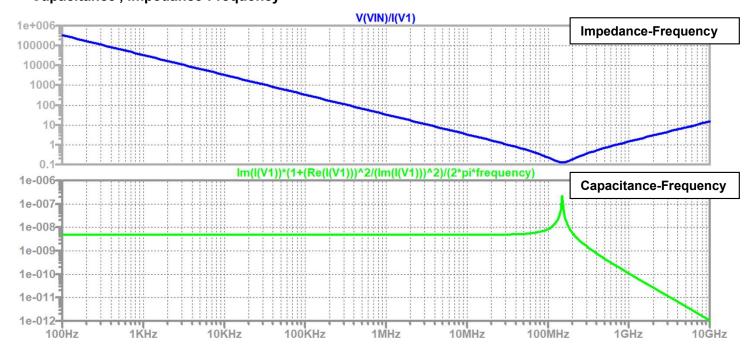
Referred to Data Sheet

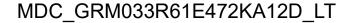


Simulation results are following.

Explanatory notes — : simulated

Capacitance, Impedance-Frequency







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