

PSpice Model NPN JCET 2SD1664



Model Information

Model Mextram model
Call Name MDC_2SD1664_PS
Pin Assign 1:C 2:B 3:E
File List Model Library MDC_2SD1664_PS01.lib
 Model Report MDC_2SD1664_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version C,Nov,2015
- Product name 2SD1664
- Company name Jiangsu Changjiang Electronics Technology Co., Ltd.
- Characteristics $I_c V_{ce}[ib], h_{FE} I_c [Temp], V_{be}(sat) I_c [Temp], V_{ce}(sat) I_c [Temp], f_T I_c [V_{ce}], C_{ob}, C_{ib}$

Simulation Range

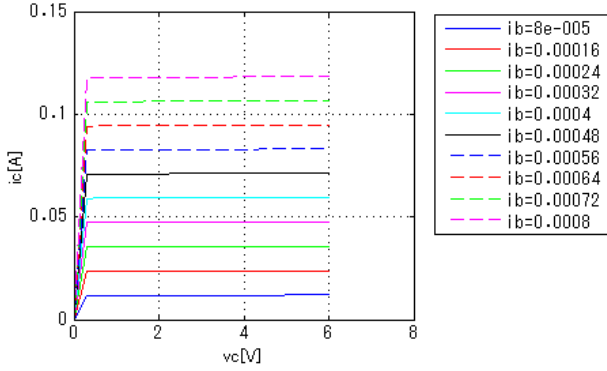
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	32	V
Base current (DC)	0	to	0.1	A
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

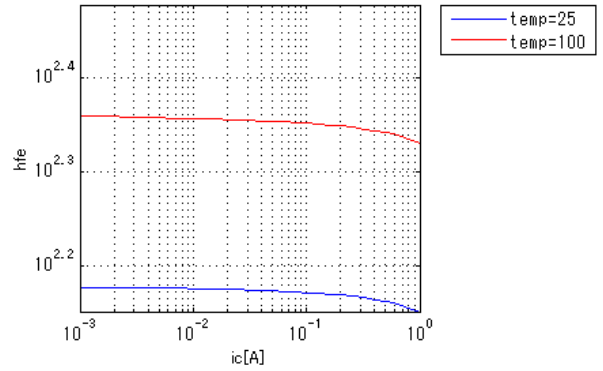
IcVce[ib]

Temp. = 25deg C



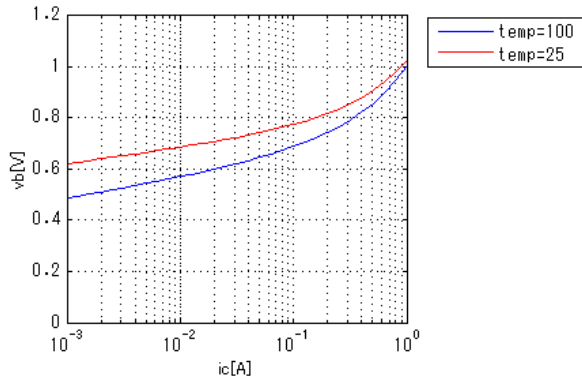
hFEIc[Temp]

Vce = 3V



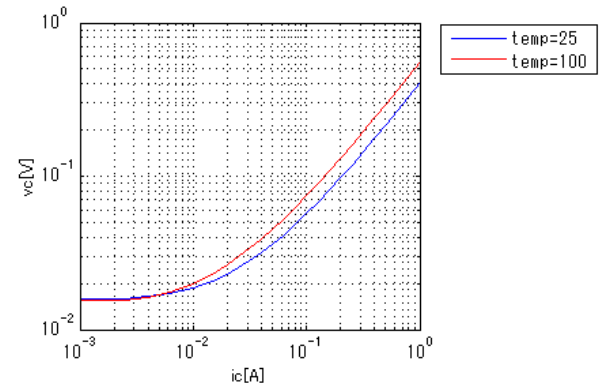
Vbe(sat)Ic[Temp]

IC/IB = 10

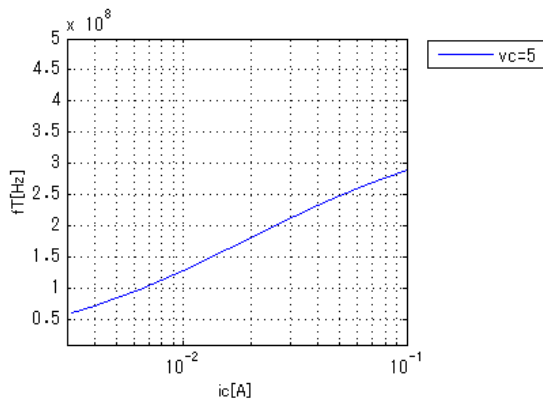


Vce(sat)Ic[Temp]

IC/IB = 10

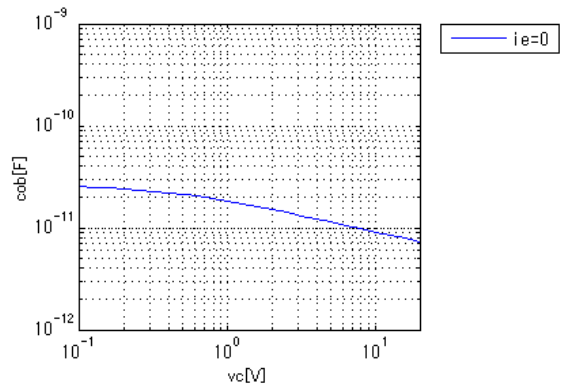


fTlc[Vce]



Cob

Freq. = 1MHz

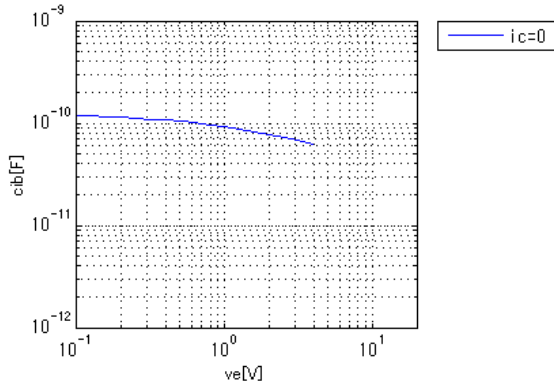


Simulation results are following.

Explanatory notes — : simulated

Cib

Freq. = 1MHz



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