

LTspice Model フォトカプラ Avago QCPL-032H-500E

Model Information

Model A macro model

Call Name MDC_QCPL-032H-500E_LT

Pin Assign 1:NC 2;ANODE 3:CATHODE 4:NC 5:VEE 6:VO 7:NC 8:VCC

File List Model Library MDC_QCPL-032H-500E_LT01.lib

Model Report MDC_QCPL-032H-500E_LT.pdf(this file)

Verified Simulator Version LTspice XVII

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version N.A.

Product name
QCPL-032H-500E

Company name Avago

[Characteristics listed]

● Characteristics IF-VF,IR-VR,Capacitance

VOH-IOH, VOL-IOL, ICCH-VCCH, ICCL-VCCL, VO-IF

TP-VCC,TP-IF,TP-Rg,TP-Cg

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



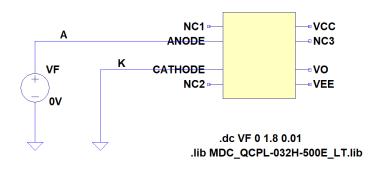
Model Functions Table

Functions	Implemented
IF-VF	0
IR-VR	0
Capacitance	0
VOH-IOH	0
VOL-IOL	0
ICCH-VCCH	0
ICCL-VCCL	0
VO-IF	0
TP-VCC	0
TP-IF	0
TP-Rg	0
TP-Cg	0



IF-VF Testbench

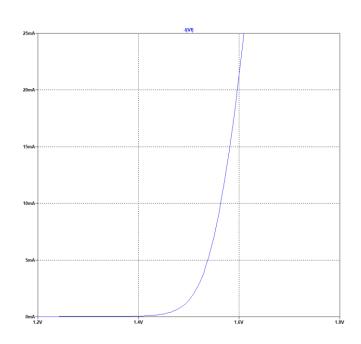
Referred to Data Sheet



Simulation results are following.

Explanatory notes — : simulated

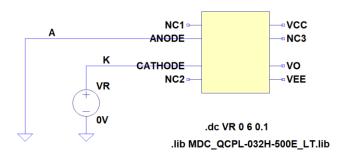
IF-VF





IR-VR Testbench

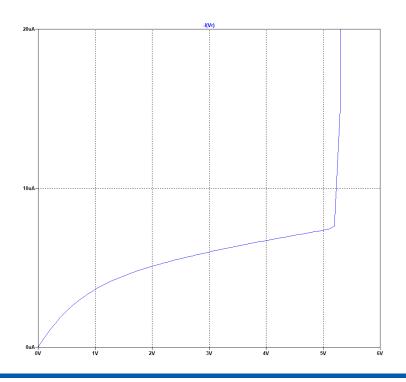
Referred to Data Sheet



Simulation results are following.

Explanatory notes — : simulated

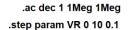
IR-VR

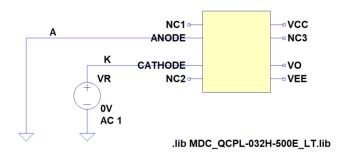




Capacitance Testbench

Referred to Data Sheet

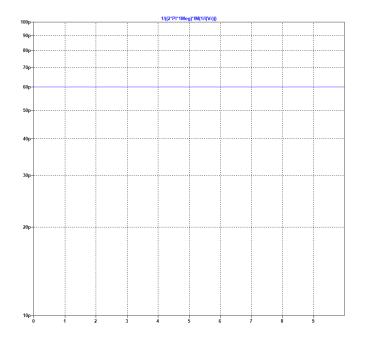




Simulation results are following.

Explanatory notes — : simulated

Capacitance

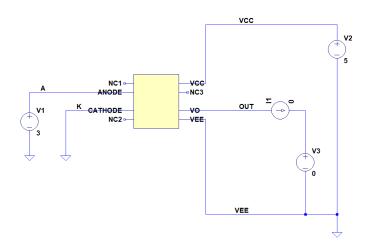




VOH-IOH Testbench

Referred to Data Sheet

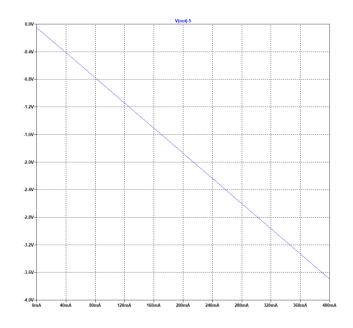
.dc | 11 0 0.4 0.01 .lib MDC_QCPL-032H-500E_LT.lib



Simulation results are following.

Explanatory notes — : simulated

VOH-IOH

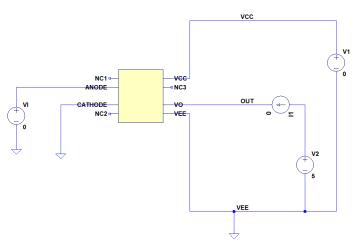




VOL-IOL Testbench

Referred to Data Sheet

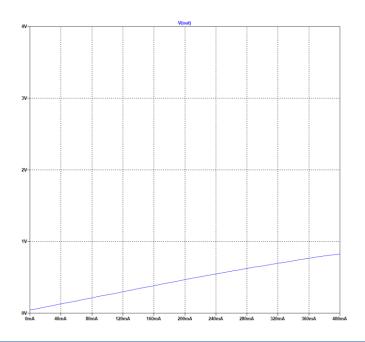
.dc |1 0 0.4 0.01 .lib MDC_QCPL-032H-500E_LT.lib



Simulation results are ronowing.

Explanatory notes — : simulated

VOL-IOL

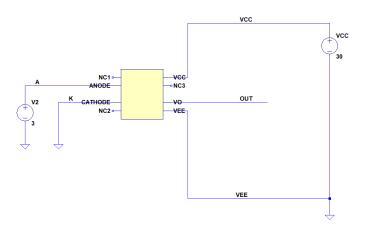




ICCH-VCCH Testbench

Referred to Data Sheet

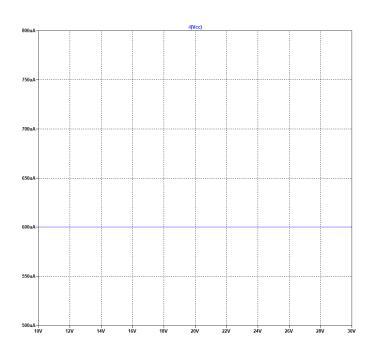
.dc VCC 10 30 0.01 .lib MDC_QCPL-032H-500E_LT.lib



Simulation results are following.

Explanatory notes — : simulated

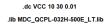
ICCH-VCCH

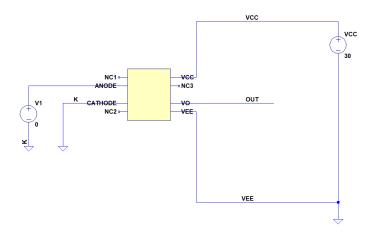




ICCL-VCCL Testbench

Referred to Data Sheet

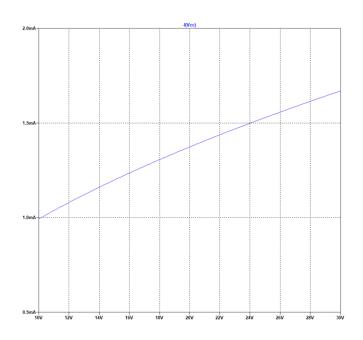




Simulation results are following.

Explanatory notes — : simulated

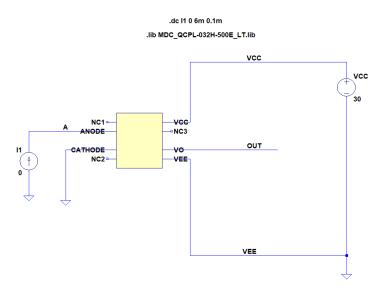
ICCL-VCCL





VO-IF Testbench

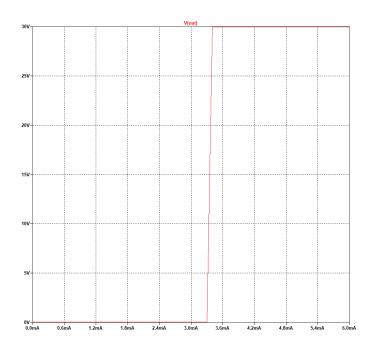
Referred to Data Sheet



Simulation results are following.

Explanatory notes — : simulated

VO-IF

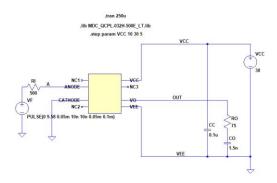




TP-VCC Testbench

Referred to Data Sheet

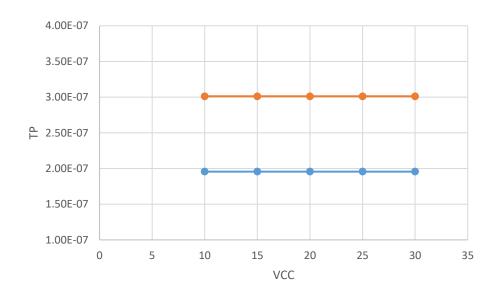
.meas TRAN TPLH TRIG -I(VF)=8m RISE=1 TARG V(out)=15 RISE=1 .meas TRAN TPHL TRIG -I(VF)=8m FALL=1 TARG V(out)=15 FALL=1



Simulation results are following.

Explanatory notes — : simulated

TP-VCC

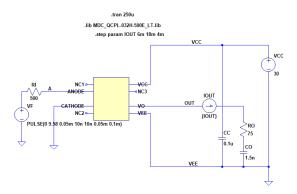




TP-IF Testbench

Referred to Data Sheet

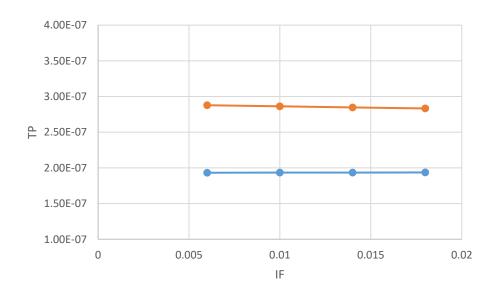
.meas TRAN TPLH TRIG -I(VF)=8m RISE=1 TARG V(out)=15 RISE=1 .meas TRAN TPHL TRIG -I(VF)=8m FALL=1 TARG V(out)=15 FALL=1



Simulation results are following.

Explanatory notes — : simulated

TP-IF



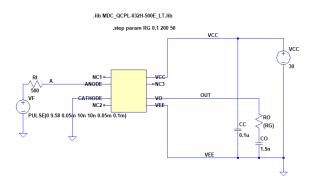


TP-Rg Testbench

Referred to Data Sheet

.meas TRAN TPLH TRIG -I(VF)=8m RISE=1 TARG V(out)=15 RISE=1 .meas TRAN TPHL TRIG -I(VF)=8m FALL=1 TARG V(out)=15 FALL=1

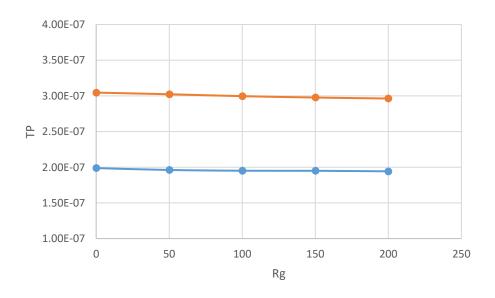
.tran 250u



Simulation results are following.

Explanatory notes — : simulated

TP-Rg

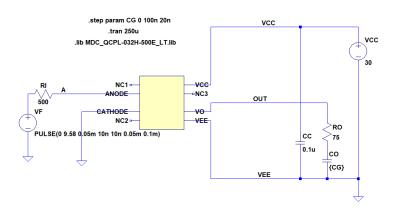




TP-Cg Testbench

Referred to Data Sheet

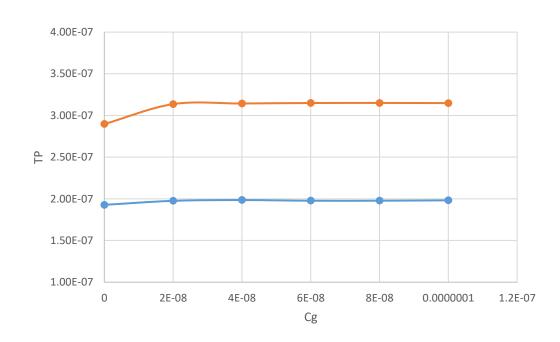
.meas TRAN TPLH TRIG -I(VF)=8m RISE=1 TARG V(out)=15 RISE=1 .meas TRAN TPHL TRIG -I(VF)=8m FALL=1 TARG V(out)=15 FALL=1



Simulation results are following.

Explanatory notes — : simulated

TP-Cg





DISCLAIMER

- 1. This SPICE (Simulation Program with Integrated Circuit Emphasis) model and its content (the "Contents") are copyright of MoDeCH Inc. All rights reserved. Any redistribution or reproduction of any or all part of the Contents in any form is prohibited without express written permission made by MoDeCH Inc.
- MoDeCH Inc. as licensor (the "Licensor") hereby grants to you, as licensee (the "Licensee"), a nonexclusive, non-transferable license to use the Contents as long as you abide by the terms and conditions of this DISCLAIMER.
- 3. The Licensee is not authorized to sell, loan, rent and redistribute or license the Contents in whole or in part, or in modified form, to anyone.
- 4. The Licensor shall in no way be liable to the Licensee or any third party for any loss or damage (including ,but not limited to, lost profits, or other incidental, consequential, or punitive damages), however caused (including through negligence) which may be directly or indirectly suffered from, arising out of, or in connection with, any use of the Contents.
- 5. Notwithstanding anything contained in this DISCLAIMER, in no event shall Licensor be liable for any claims, damages or loss which may arise from the modification, combination, operation or use of the Contents with the Licensee's computer programs.
- 6. The Licensor does not warrant that the Contents will function in any environment.
- 7. The Contents may be changed or updated without notice. MoDeCH Inc. may also make improvements and/or changes in the products, pricing and/or the programs related to the Contents at any time without notice.



MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/

Aug 23,2021 Rev 1.0