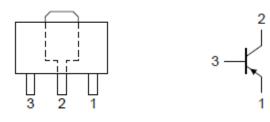


PSpice Model PNP Nexperia BCX51



Model Information

Model Call Name	Gummel-Poon model MDC BCX51 PS
Pin Assign	1:E 2:C 3:B
File List	Model Library Model Report

MDC_BCX51_PS01.lib MDC_BCX51_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

Rev. 9 - 13 October 2011 BCX51 Nexperia B.V. hFEIc[Temp],IcVce[ib],VbeIc[Temp],Vce(sat)Ic[Temp],CobVc b,fTIc[Vce]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-45	V
Collector current (DC)	0	to	-1	А
Temperature	-65	to	150	deg C



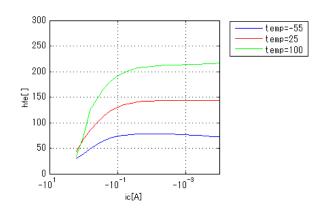
BJT		O : Implemented × : Not Implemented — : Not applicable	
Model Functions Table	RANK=1		
Functions	RANK	Implemented	
IC-VBE(Temp)	1	0	
IC-VCE-IB(Temp)	1	0	
IC-hFE(Temp)	1	0	
VCE(sat)-IC	1	0	
VBE(sat)-IC	1	—	
Capacitance	1	0	
Transition	1	0	
Switching	1	—	



Simulation results are following. Explanatory notes — : simulated

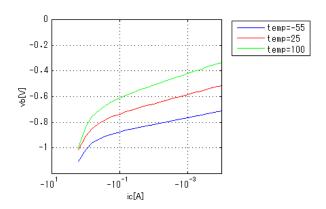
hFElc[Temp]

Vce = -2V



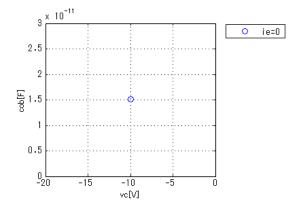
Vbelc[Temp]

Vce = -2V





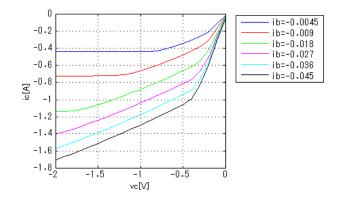
Freq = 1000000Hz



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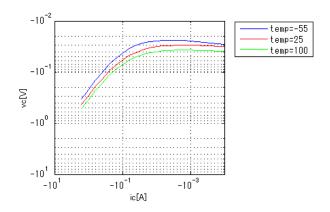
lcVce[ib]

Temp = 25degC



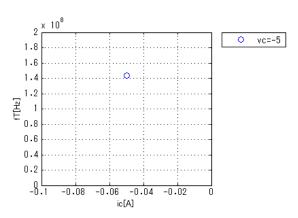
Vce(sat)lc[Temp]

IC/IB = 10





Freq = 10000000Hz





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