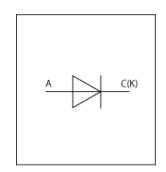


PSpice Model ESD Protection Diode Panasonic EZAEG3A50AV



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_EZAEG3A50AV_PS

Pin Assign 1:A 2:C

File List Model Library MDC_EZAEG3A50AV_PS01.lib

Model Report MDC_EZAEG3A50AV_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Jul. 2014

Product name
EZAEG3A50AV

Company name Panasonic Corporation

Characteristics IrVr(Breakdown)

Simulation Range

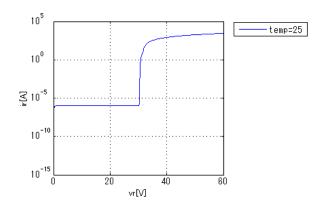
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	60	V
Temperature	-55	to	125	deg C



Simulation results are following. Explanatory notes — : simulated

IrVr(Breakdown)





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