

MDC_RD47FS_PS

PSpice Model Zener Diode RENESAS RD47FS

Model Information

ModelA macro model based on general SPICE diode modelCall NameMDC_RD47FS_PSPin Assign1:A 2:CFile ListModel LibraryMDC_RD47FS_PS01.libModel ReportMDC_RD47FS_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

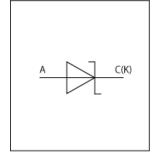
Date/Version
Product name
Company name
Characteristics

August 2008 RD47FS Renesas Electronics Corporation BvTemp,IrVr(Breakdown)

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

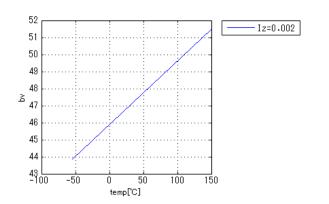
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	93	V
Temperature	-55	to	150	deg C



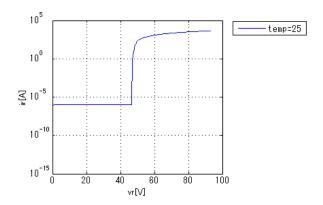


Simulation results are following. Explanatory notes -: simulated

BvTemp



IrVr(Breakdown)





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