

LTspice Model

ESD Protection Diode

Nexperia

PESD24VS1UL



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_PESD24VS1UL_LT
Pin Assign 1:A 2:C
File List Model Library MDC_PESD24VS1UL_LT01.lib
 Model Report MDC_PESD24VS1UL_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 3
- Product name PESD24VS1UL
- Company name Nexperia B.V.
- Characteristics CjVr,IrVr(Breakdown)

Simulation Range

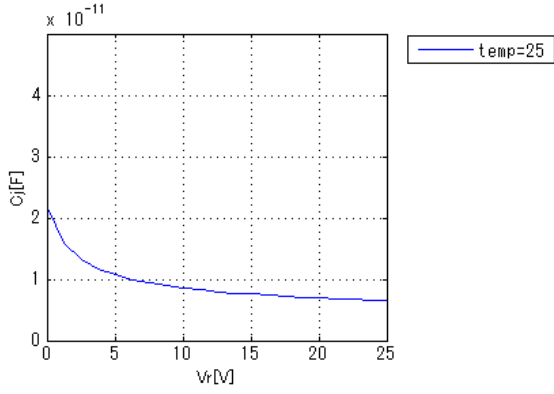
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	54	V
Temperature	-65	to	150	deg C

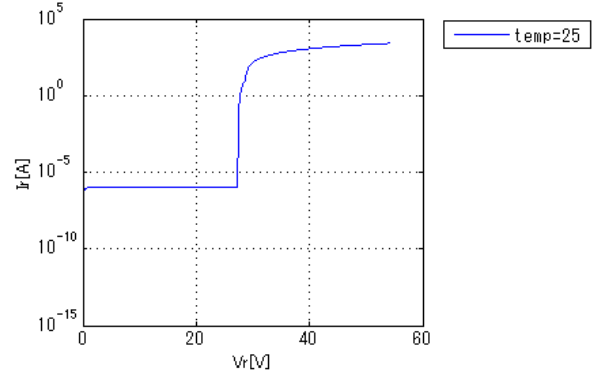
Simulation results are following.
Explanatory notes — : simulated

CjVr

Freq. = 1MHz



IrVr(Breakdown)



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