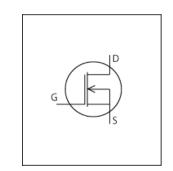


LTspice Model NMOS RENESAS 2SK2225



Model Information

Model A macro model based on BSIM3 model

Call Name MDC_2SK2225_LT

Pin Assign 1:G 2:D 3:S

File List Model Library MDC_2SK2225_LT01.lib

Model Report MDC_2SK2225_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct name2017.12.062SK2225

● Company name Renesas Electronics Corporation

● Characteristics IdVds[Vgs],IdVgs[Temp],VdsVgs[Id],Rds(on)Id[Vgs],Rds(on)

Temp[Id], YfsId[Temp], TrrIf[Ir], Capacitance Vds[Cname], VgsQg[Vdd], VdsQg[Vdd], SwitchingIdd[Tname]Rs, TrrWaveform, S

witchingWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

ltem	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	1,500	V
Gate-source voltage (DC)	-20	to	20	V
Temperature	-55	to	150	deg C



MOSFET

O: Implemented

×: Not Implemented
—: Not applicable

Model Functions Table

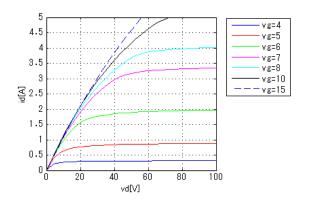
	KANK=1	
Functions	RANK	Implemented
ID-VDS-VGS	1	0
ID-VGS(Temp)	1	0
RDS(on)	1	0
Capacitance	1	0
Gate Charge	1	0
IS-VSD(Forward)	1	0
Reverse recovery	1	0
Switching(Typ.)	1	0
Bv	1	_
Yfs	1	0
Vth	1	_



Simulation results are following. Explanatory notes — : simulated

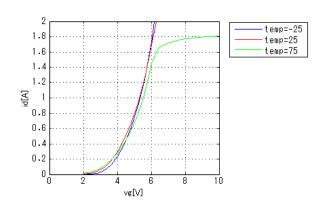
IdVds[Vgs]

Temp = 25degC

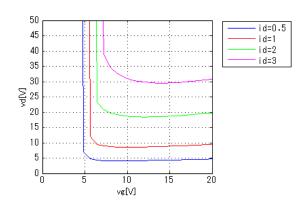


IdVgs[Temp]

Vds = 25V

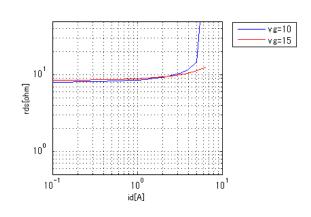


VdsVgs[Id]



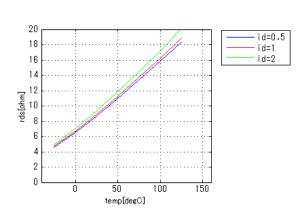
Rds(on)Id[Vgs]

Temp = 25degC



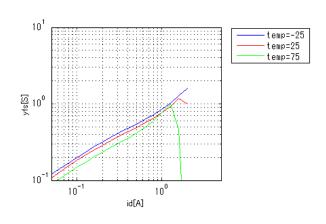
Rds(on)Temp[Id]

Vgs = 15V



Yfsld[Temp]

Vds = 25V

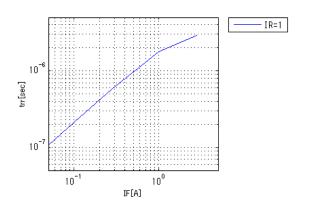




Simulation results are following. Explanatory notes — : simulated

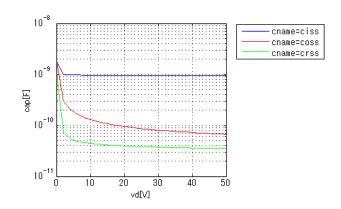
Trrlf[lr]

vdd = 1200V, didt = 100A/us, Temp = 25degC, IF = IR



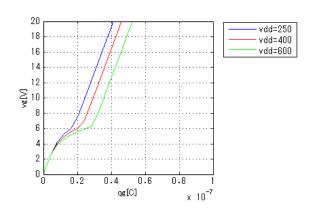
Capacitance Vds[Cname]

freq = 1000000Hz



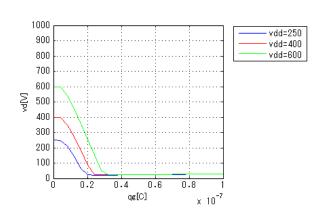
VgsQg[Vdd]

Id = 2.5A



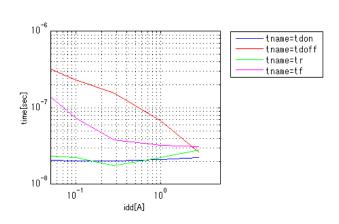
VdsQg[Vdd]

Id = 2.5A

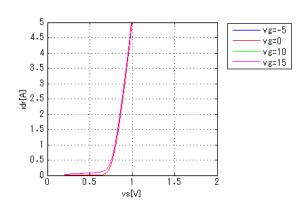


SwitchingIdd[Tname]Rs

vgg = 10V, vdd = 30V, RGS = 50ohm



IsVsd[Vgs]

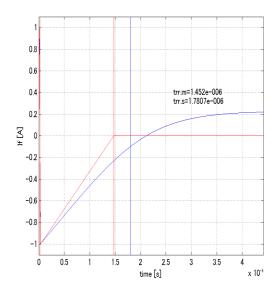




Simulation results are following. Explanatory notes — : simulated

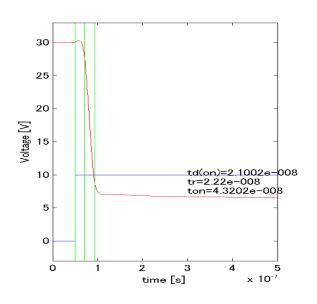
Trr Waveform (Red: Datasheet Blue: Simulation)

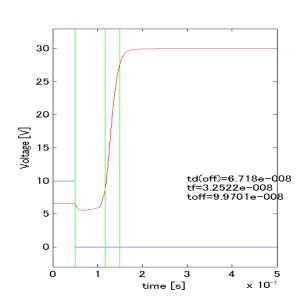
didt = 100A/us, vdd = 1200V, if = 1A, ir = 1A



Switching Waveform (Blue : INPUT Red : OUTPUT)

vgg = 10V, vdd = 30V, RGS = 50ohm, Temp = 25degC, Id = 1A







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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/