

LTspice Model Resetlc Rohm **BD45305**

Model Information

Model	A macro model	
Call Name	MDC_BD45305	5_LT
Pin Assign	1:ER 2:SUB 3:GND 4:VOUT 5:VDD	
File List	Model Library	MDC_BD45305_LT01.lib
	Model Report	MDC_BD45305_LT.pdf(this file)

Verified Simulator Version

LTspiceXVII

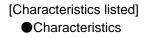
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version	N/A
Product name	BD45305
Company name	Rohm



I/O, Output delay time, Transient

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



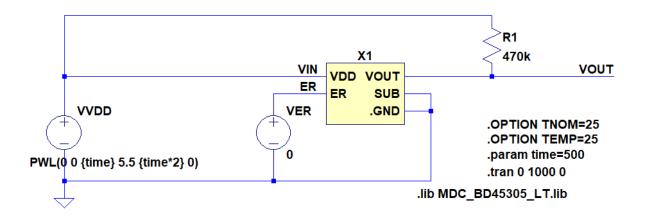
Model Functions Table

Functions	Implemented
1/0	0
Output delay time	0
Transient	0



I/O Testbench

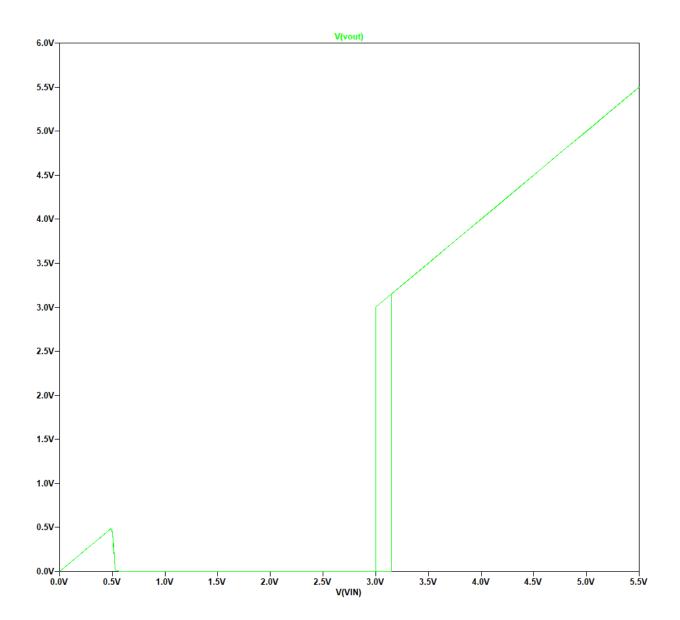
Referred to Data Sheet





Simulation results are following. Explanatory notes — : simulated

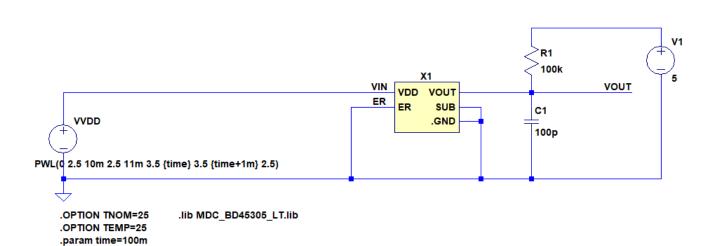
I/O





Output delay time Testbench

Referred to Data Sheet

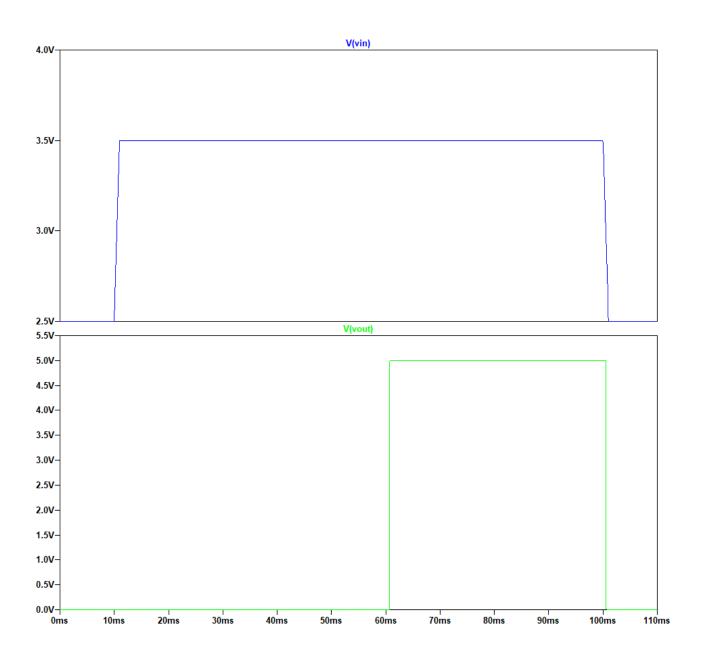


.tran 0 110m 0



Simulation results are following. Explanatory notes -: simulated

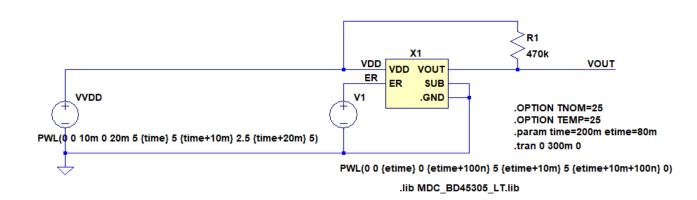
Output delay time





Transient Testbench

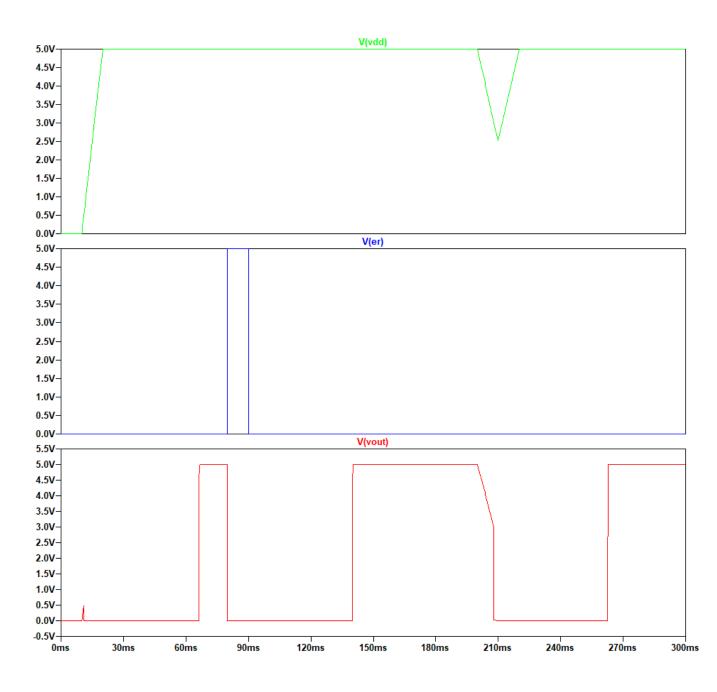
Referred to Data Sheet





Simulation results are following. Explanatory notes — : simulated

Transient





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