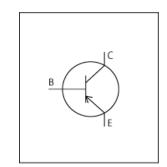


# PSpice Model PNP ON NSS12200WT1G



## **Model Information**

ModelGummel-Poon modelCall NameMDC\_NSS12200WT1G\_PS

Pin Assign 1:C 2:B 3:E

File List Model Library MDC\_NSS12200WT1G\_PS02.lib

Model Report MDC\_NSS12200WT1G\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev.2

Product nameCompany nameNSS12200WT1GON Semiconductor.

● Characteristics Vcelb[Ic], Vce(sat)Ic[hFE], hFEIc[Temp], IcVbe[Temp], Vbe(sat

)Ic[hFE]

#### **Simulation Range**

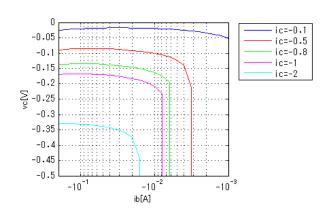
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-12	V
Base current (DC)	0	to	-0.20	Α
Temperature	-55	to	150	deg C

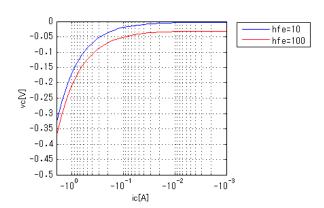


Simulation results are following. Explanatory notes — : simulated

## Vcelb[lc]

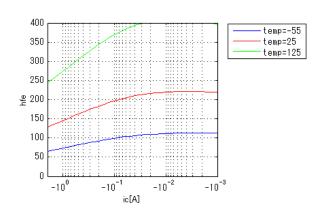


## Vce(sat)lc[hFE]



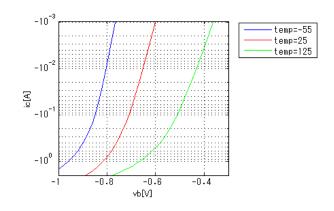
#### hFEIc[Temp]

Vce = -1.5V

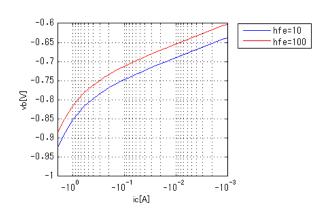


#### IcVbe[Temp]

Vce = -1.5V



## Vbe(sat)Ic[hFE]





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