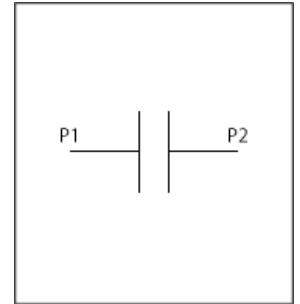


PSpice Model

Ceramic Capacitor

TDK

C1005C0G1H221J050BA



Model Information

Model An original macro model
Call Name MDC_C1005C0G1H221J050BA_PS
Pin Assign 1:P1 2:P2
File List Model Library MDC_C1005C0G1H221J050BA_PS01.lib
 Model Report MDC_C1005C0G1H221J050BA_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Measured]

- Date 6/20/2016
- Company name TDK Corporation
- Characteristics Cap.Freq.,Imp.Freq.,EsrFreq.

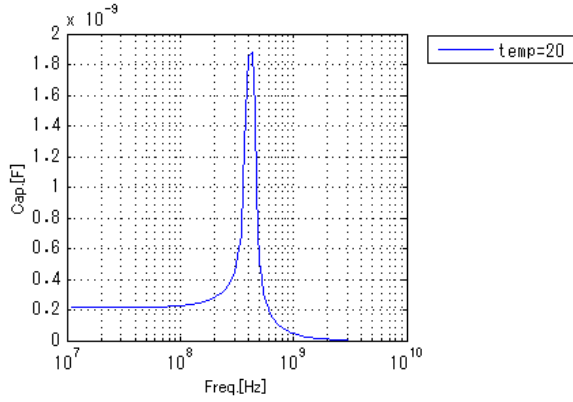
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

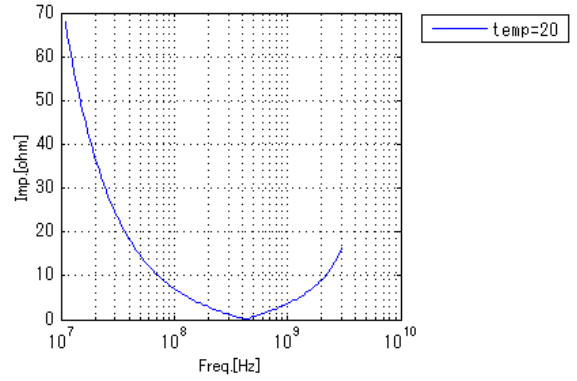
Item	Range			Unit
	Min.	to	Max.	
Frequency	10,943,100	to	3,000,000,000	Hz
Temperature	20	to	20	deg C

Simulation results are following.
 Explanatory notes — : simulated

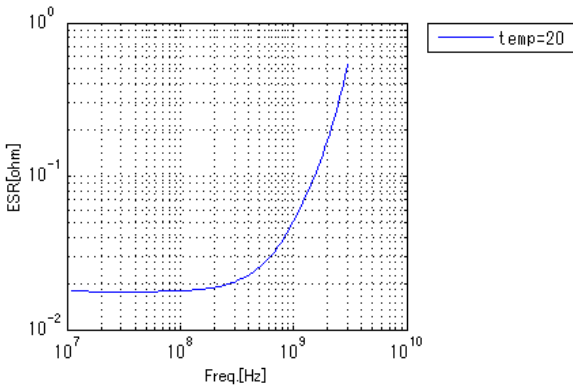
Cap.Freq.



Imp.Freq.



EsrFreq.



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