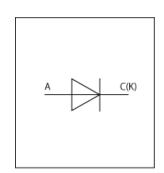


# PSpice Model ESD Protection Diode HITACHI V17E



## **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC\_V17E\_PS

Pin Assign 1:A 2:C

File List Model Library MDC\_V17E\_PS01.lib

Model Report MDC\_V17E\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version N/AProduct name V17E

Company name Hitachi Power Semiconductor Device, Ltd.

Characteristics IfVf[Temp],IrVr(Breakdown)

#### Simulation Range

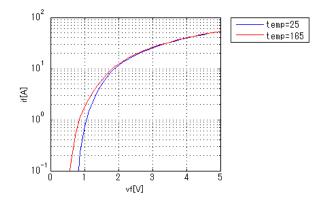
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	50	V
Reverse Voltage	0	to	800	V
Temperature	-40	to	165	deg C

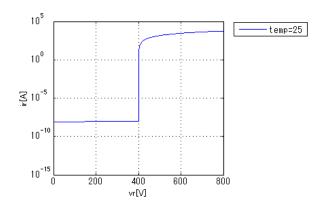


Simulation results are following. Explanatory notes — : simulated

## IfVf[Temp]



## IrVr(Breakdown)





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MoDeCH Inc.

**Head Office** 

Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/