

# LTspice Model

## Logic IC

### TOSHIBA

### 74VHC14FT

#### Model Information

**Model** An original macro model  
**Call Name** MDC\_74VHC14FT\_LT  
**Pin Assign** 1:1A 2:1Y 3:2A 4:2Y 5:3A 6:3Y 7:GND 8:4Y 9:4A 10:5Y 11:5A 12:6Y 13:6A 14:VCC  
**File List** Model Library MDC\_74VHC14FT\_LT01.lib  
 Model Report MDC\_74VHC14FT\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII

**Note** Gate Model Call Name is "MDC\_74VHC14FT\_GATE\_LT", Gate Model Pin Assign is "1:A 2:Y 3:VCC 4:GND"

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev.3.0
- Product name 74VHC14FT
- Company name Toshiba Corporation
- Characteristics Transient

#### Simulation Condition

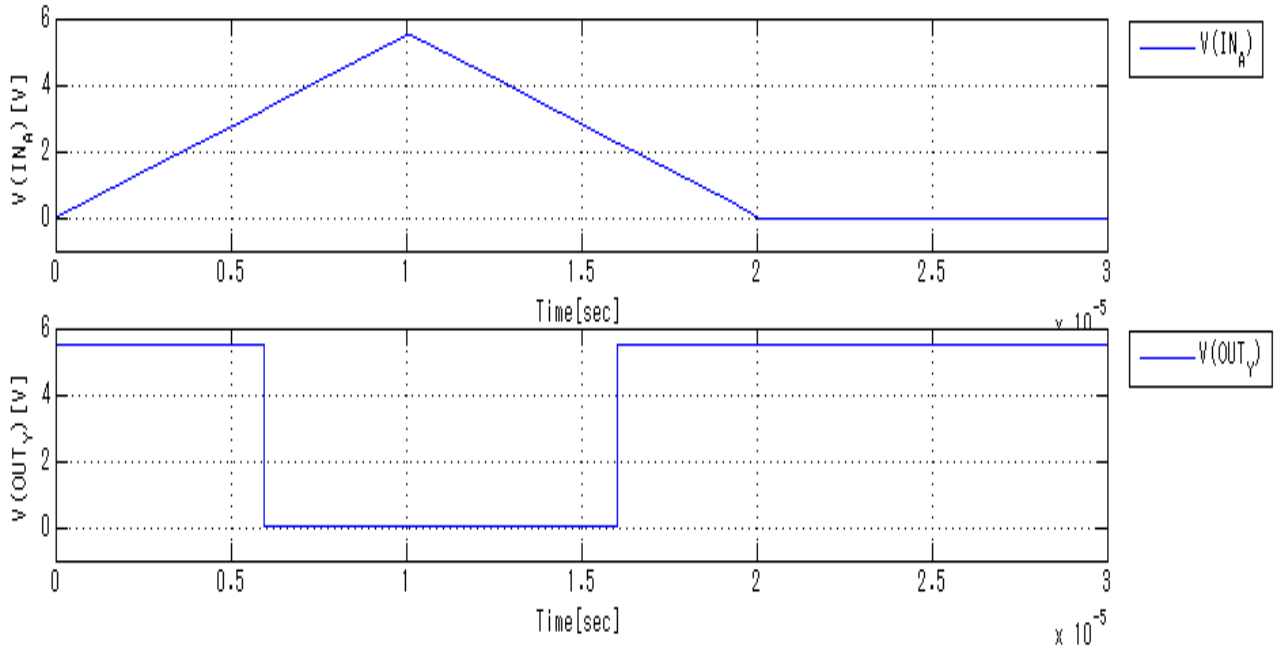
This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

Simulation results are following.  
Explanatory notes — : simulated

**Transient**

Temp. = 25deg C



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