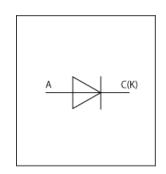


LTspice Model Rectifier Diode VISHAY AS1PJHM3



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_AS1PJHM3_LT

Pin Assign 1:A 2:C

File List Model Library MDC_AS1PJHM3_LT01.lib

Model Report MDC_AS1PJHM3_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 27-Jul-15Product name AS1PJHM3

Company nameCharacteristicsVishay Intertechnology, Inc.IfVf[Temp],IrVr[Temp],CjVr

Simulation Range

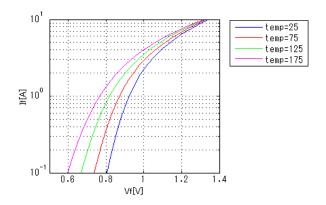
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-----------------|-------|----|-------|-------|
| | Min. | | Max. | |
| Forward Voltage | 0 | to | 14 | V |
| Reverse Voltage | 0 | to | 1,200 | V |
| Temperature | -55 | to | 175 | deg C |

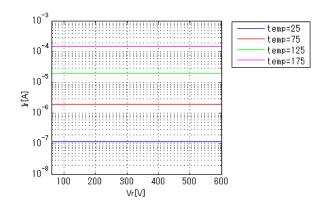


Simulation results are following. Explanatory notes — : simulated

IfVf[Temp]

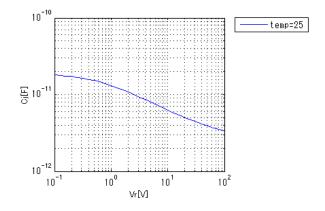


IrVr[Temp]



CjVr

Freq. = 1MHz





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