

# PSpice Model

## NPN

## ON

## FJAFS1510A



### Model Information

**Model** Gummel-Poon model  
**Call Name** MDC\_FJAFS1510A\_PS  
**Pin Assign** 1:C 2:B 3:E  
**File List** Model Library MDC\_FJAFS1510A\_PS02.lib  
 Model Report MDC\_FJAFS1510A\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 16.6  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. A2
- Product name FJAFS1510A
- Company name ON Semiconductor.
- Characteristics  $I_c V_{ce}[I_b], h_{FE} I_c [Temp], V_{ce}(sat) I_c [Temp], I_c V_{be} [Temp], C_{ob}$

### Simulation Range

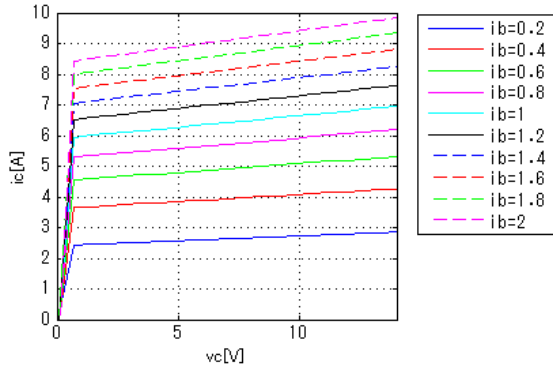
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	750	V
Base current (DC)	0	to	2	A
Temperature	-55	to	150	deg C

Simulation results are following.  
 Explanatory notes — : simulated

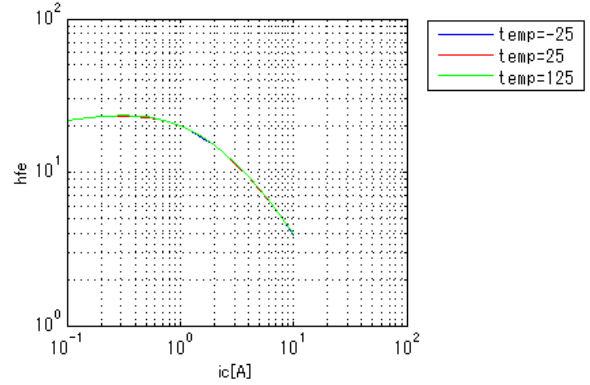
**IcVce[ib]**

Temp. = 25deg C



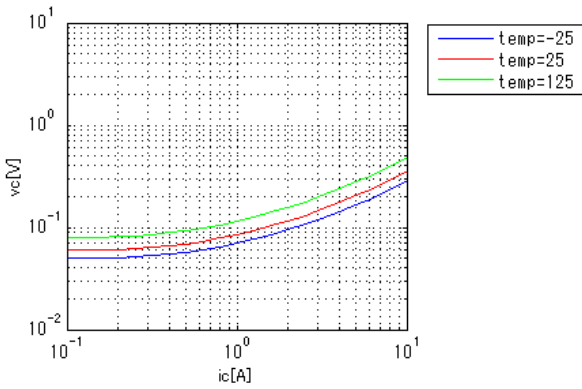
**hFEIc[Temp]**

Vce = 5V



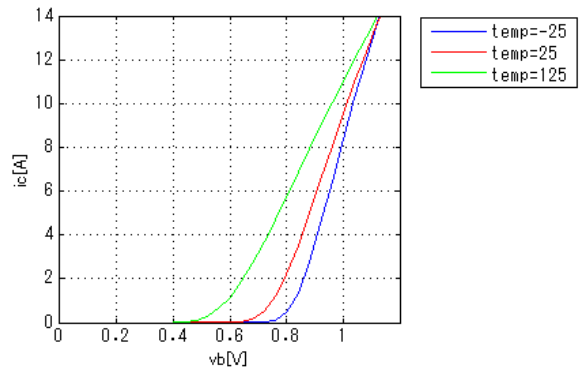
**Vce(sat)Ic[Temp]**

IC/IB = 3



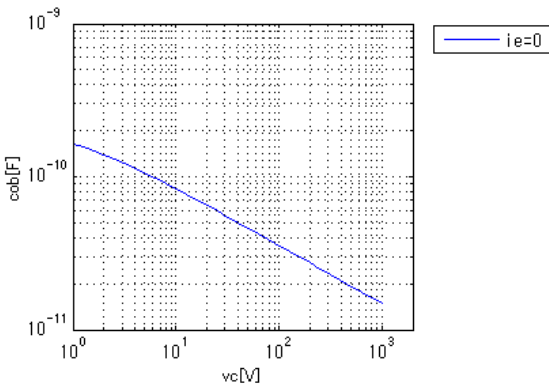
**IcVbe[Temp]**

Vce = 5V



**Cob**

Freq. = 1MHz



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