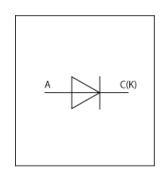


# PSpice Model ESD Protection Diode VISHAY TPSMP36AHM3



## **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC\_TPSMP36AHM3\_PS

Pin Assign 1:A 2:C

File List Model Library MDC\_TPSMP36AHM3\_PS01.lib

Model Report MDC\_TPSMP36AHM3\_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6

Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 08-Oct-12Product name TPSMP36AHM3

Company nameCharacteristicsVishay Intertechnology, Inc.BvTemp,IrVr(Breakdown)

#### Simulation Range

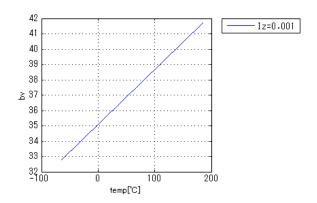
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	72	V
Temperature	-65	to	185	deg C

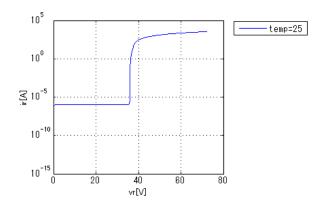


Simulation results are following. Explanatory notes — : simulated

## **BvTemp**



## IrVr(Breakdown)





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