

PSpice Model Capacitor Rubycon 25PJV470MAINVB10X10.5

Model Information

Model A macro model

Call Name MDC_25PJV470MAINVB10X10.5_PS

Pin Assign

File List Model Library MDC_25PJV470MAINVB10X10_5_PS.lib

Model Report MDC_25PJV470MAINVB10X10.5_PS.pdf(this file)

Verified Simulator Version PSpice 17.2

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version

Product nameCompany name25PJV470M10X10.5Rubycon Corporation

[Characteristics listed]

CharacteristicsCapacitance , Impedance - Frequency

Simulation Condition

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	20	deg C

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Rev 1.0

1



MDC_25PJV470MAINVB10X10.5 _PSLT

O:Implemented ×:Not Implemented

—: Not applicable

Model Functions Table

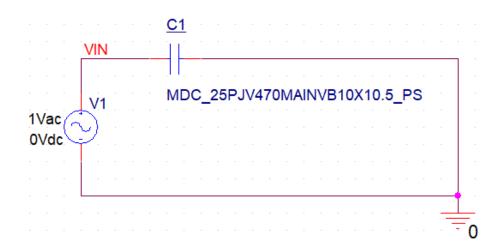
	10 (141)	
Functions	RANK	Implemented
Impedance-Frequency	1	0
Capacitance-Frequency	1	0

RANK=1



Capacitance, Impedance - Frequency Testbench

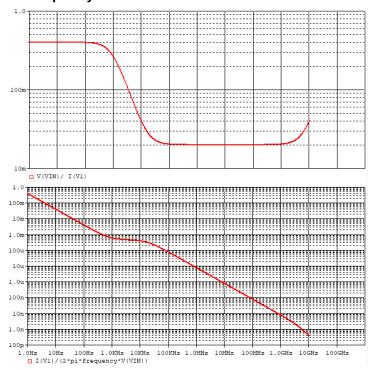
Referred to Data Sheet



Simulation results are following.

Explanatory notes — : simulated

Capacitance, Impedance - Frequency





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