

# PSpice Model BRT (Bias Resistor Transistor) ROHM DTA114E

# **Model Information**

 Model
 A macro model based on Mextram model

 Call Name
 MDC\_DTA114E\_PS

 Pin Assign
 1:C 2:B 3:E

 File List
 Model Library
 MDC\_DTA114E\_PS02.lib

 Model Report
 MDC\_DTA114E\_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet] • Date/Version • Product name • Company name

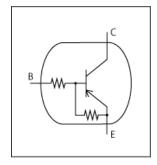
Characteristics

Unknown DTA114E ROHM Co., Ltd. IcVce[li],IcVi[OFF],IcVi[ON],hFEIc[Temp],Vce(sat)Ic[Temp]

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item                           | Range |    |      | Unit  |
|--------------------------------|-------|----|------|-------|
|                                | Min.  |    | Max. |       |
| Collector-emitter voltage (DC) | 0     | to | -50  | V     |
| Base current (DC)              | 0     | to | -5   | А     |
| Temperature                    | -55   | to | 150  | deg C |

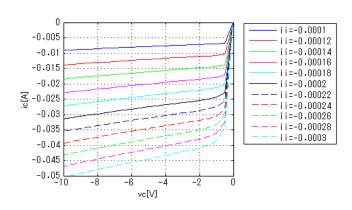




#### Simulation results are following. Explanatory notes — : simulated

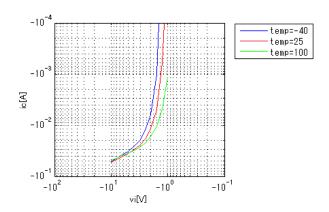
#### lcVce[li]





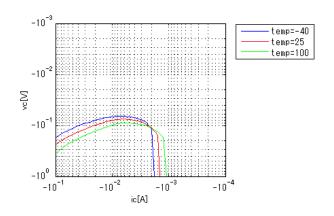
IcVi[ON]

Vce = -0.3V



## Vce(sat)lc[Temp]

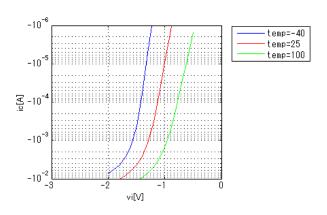
IC/IB = 20



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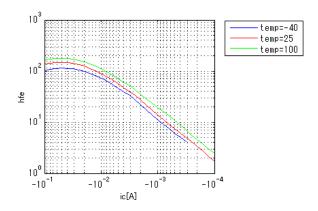
## lcVi[OFF]

Vce = -5V



## hFElc[Temp]

Vce = -5V





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