

LTspice Model

Multilayer Inductor

TDK

HPL8012060F1R22MRD3P



Model Information

Model	An original macro model		
Call Name	MDC_HPL8012060F1R22MRD3P_LT		
Pin Assign	1:1 2:2		
File List	Model Library	MDC_HPL8012060F1R22MRD3P_LT01.lib	
	Model Report	MDC_HPL8012060F1R22MRD3P_LT.pdf (this file)	
Verified Simulator Version	LTspice version XVII		
Note			

References

The information which was used for modeling is as follow:

[Data Sheet]

● Date/Version	Unknown
● Product name	HPL8012060F1R22MRD3P
● Company name	TDK Corporation
● Characteristics	ImpedanceFreq[Vf],ESRFreq[Vf],InductanceFreq[Vf],QfactorFreq[Vf],InductanceI[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-55	to	155	deg C

Inductor

○ : Implemented
 × : Not Implemented
 — : Not applicable

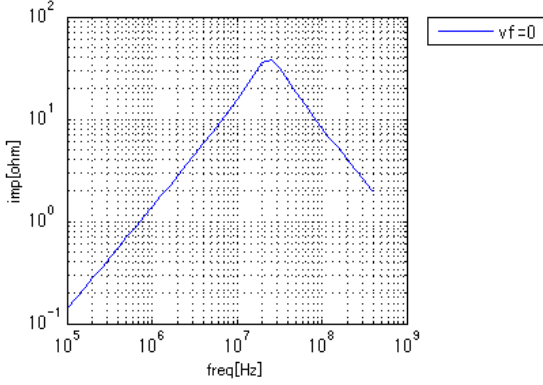
Model Functions Table
RANK=1

Functions	RANK	Implemented
Impedance-Frequency	1	○
ESR-Frequency	1	○
Inductance-Frequency	1	○
Inductance-Current	1	○
Inductance-Temp	1	—
Resistance-Voltage	1	—
Qfactor-Frequency	1	○

Simulation results are following.
 Explanatory notes — : simulated

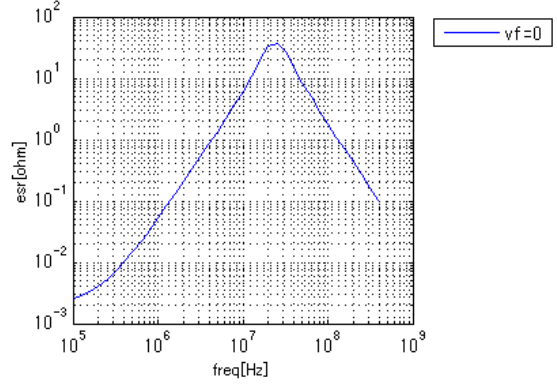
ImpedanceFreq[Vf]

temp = 20degC



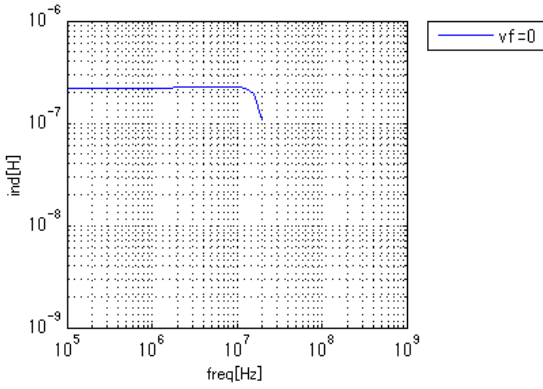
ESRFreq[Vf]

temp = 20degC



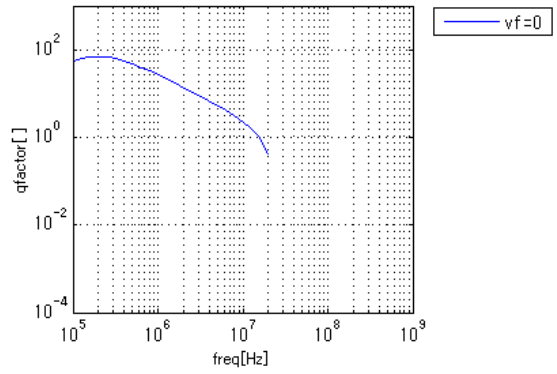
InductanceFreq[Vf]

temp = 20degC



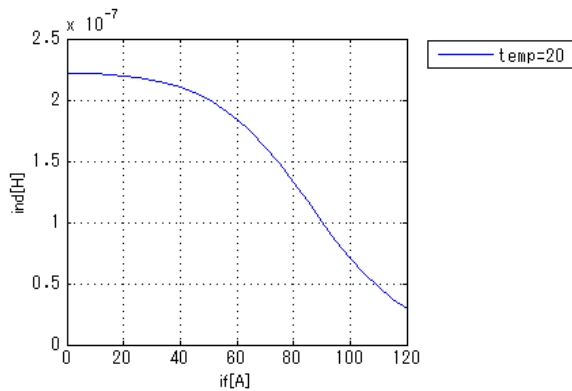
QfactorFreq[Vf]

temp = 20degC



InductanceIf[Temp]

freq = 1000000Hz



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