

# LTspice Model

## Rectifier Diode

### VISHAY INTERTECH

### BYW52



#### Model Information

**Model** A macro model based on general SPICE diode model  
**Call Name** MDC\_BYW52\_LT  
**Pin Assign** 1:A 2:C  
**File List** Model Library MDC\_BYW52\_LT01.lib  
 Model Report MDC\_BYW52\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 1.9, 21-Feb-18
- Product name BYW52
- Company name Vishay Intertechnology, Inc.
- Characteristics IfVf[Temp],CjFreq[Vf],CjVr,Trrflr,Qrrflr,TrrWaveform,QrrWaveform,SurgeWaveform

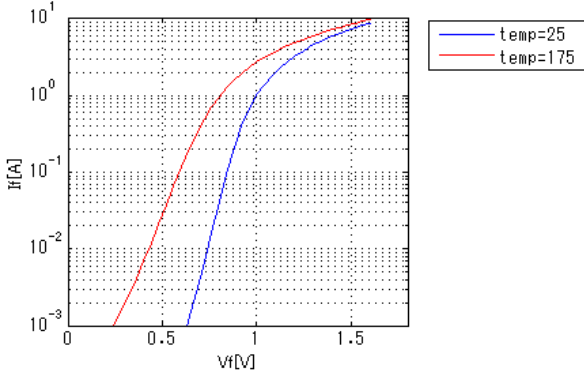
#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

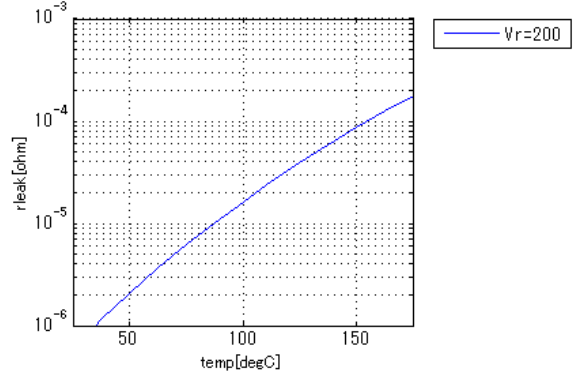
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	200	V
Temperature	-55	to	175	deg C

Simulation results are following.  
 Explanatory notes — : simulated

**IfVf[Temp]**

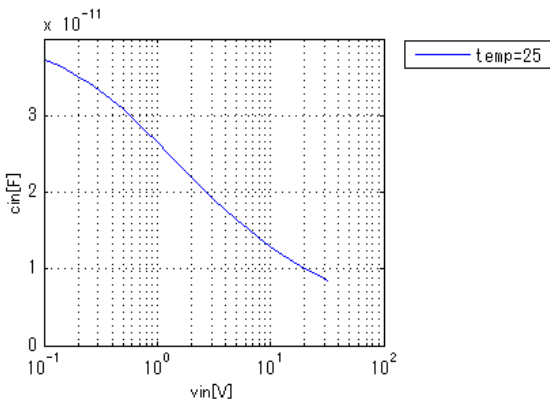


**CjFreq[Vf]**

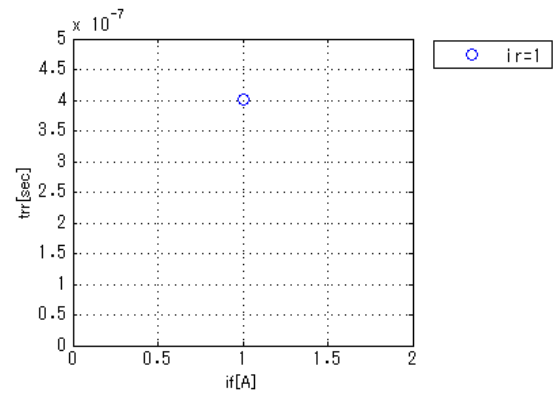


**CjVr**

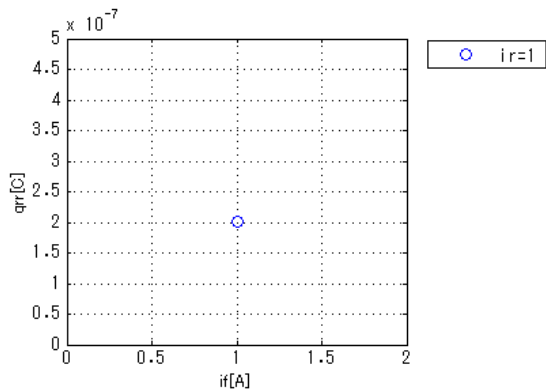
Freq. = 1MHz



**Trrlfir**

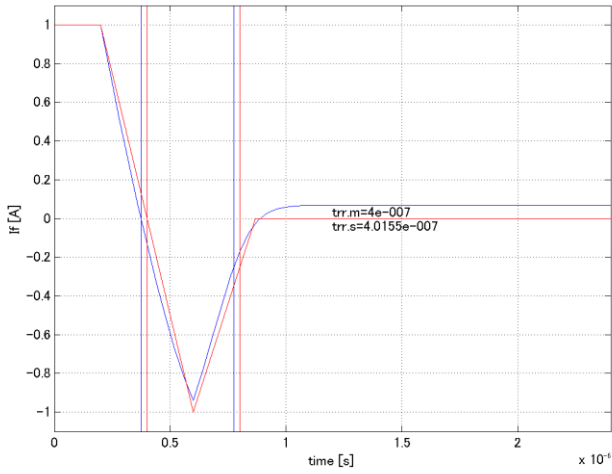


**Qrrlfir**

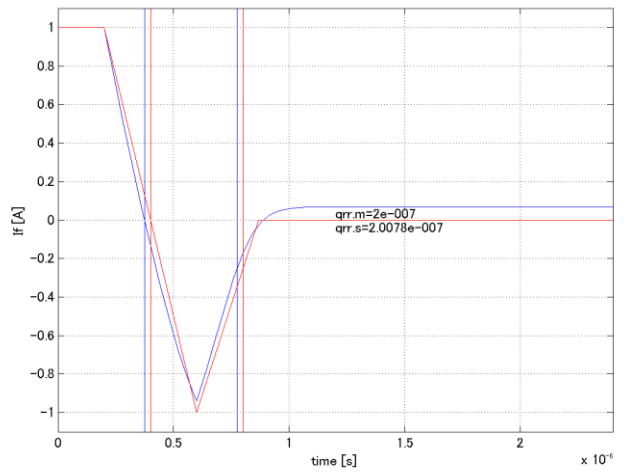


Simulation results are following.  
 Explanatory notes — : simulated

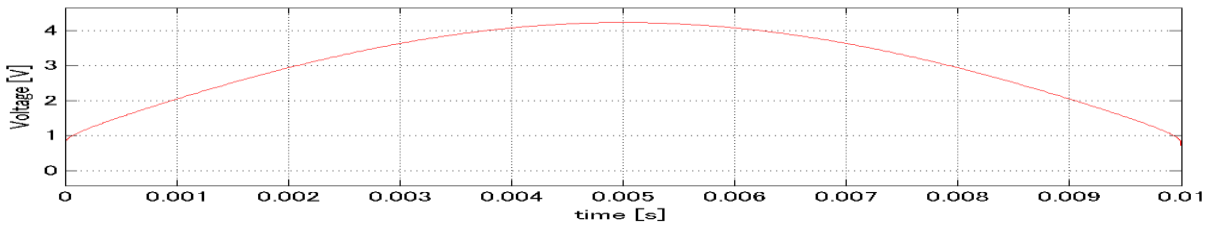
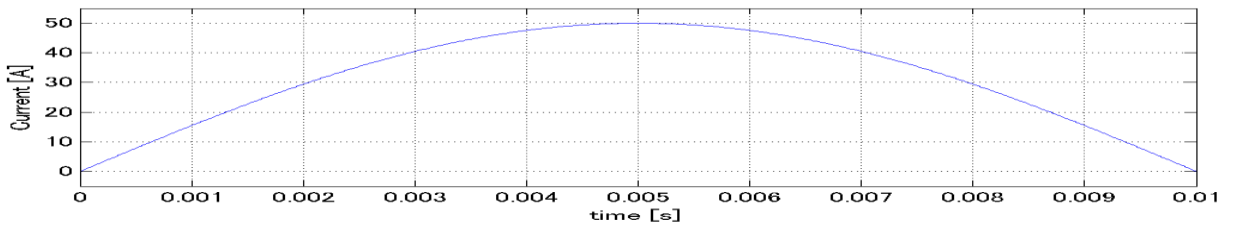
**Trr Waveform**



**Qrr Wavrfom**



**Surge Waveform (Forward)**



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