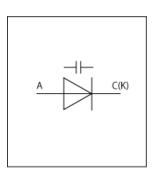


PSpice Model Variable Capacitance Diode TOSHIBA 1SV229



Model Information

ModelA macro model based on general SPICE diode modelCall NameMDC_1SV229_PSPin Assign1:A 2:CFile ListModel LibraryMDC_1SV229_PS01.libModel ReportMDC_1SV229_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

The information which was used for modeling is as follow:

[Data Sheet]

| Date/Version |
|-----------------|
| Product name |
| Company name |
| Characteristics |

2014-03-01 1SV229 Toshiba Corporation IrVr[Temp],CjVr

Simulation Range

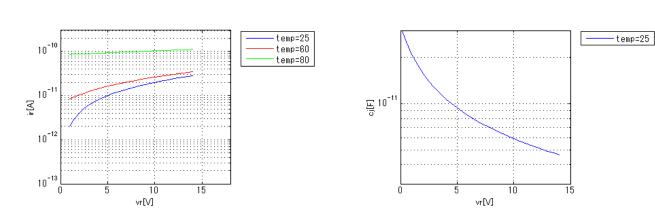
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|-----------------|-------|----|------|-------|
| | Min. | | Max. | |
| Reverse Voltage | 0 | to | 30 | V |
| Temperature | -55 | to | 125 | deg C |



Simulation results are following. Explanatory notes -: simulated

IrVr[Temp]



CjVr Freq. = 1MHz



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