

# LTspice Model Low-Side N-Channel Controller Texas Instruments Inc. LM3478MM

### **Model Information**

 Model
 A macro model

 Call Name
 MDC\_LM3478MM\_LT

 Pin Assign
 1:ISEN 2:COMP 3:FB 4:AGND 5:PGND 6:DR 7:FA/SD 8:VIN

 File List
 Model Library
 MDC\_LM3478MM\_LT02.lib

 Model Report
 MDC\_LM3478MM\_LT.pdf(this file)

 Verified Simulator Version
 LTspice XVII

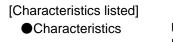
Note

#### References

The information which was used for modeling is as follow:

[Data Sheet] ●Date/Version ●Product name ●Company name

17-Jul-2020 LM3478MM Texas Instruments Inc.



UVLO, OCP, OVP Frequency adjust

#### **Simulation Condition**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

ltem	Condition			l loit
	Min	Тур	Max	Unit
VDD	2.97		40.00	V
Temperature		25		deg C

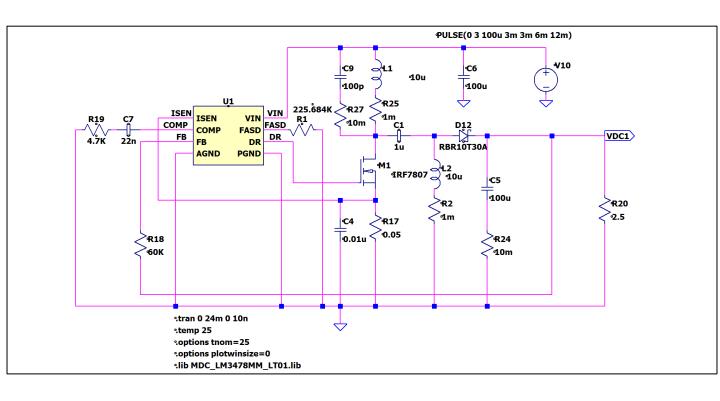


#### **Model Functions Table**

Functions	Implemented
Output Voltage Load Regulation	0
Input Undervoltage Lock-out	0
Input Undervoltage Lock-out Hysteresis	0
Short-Circuit Current Limit Sense Voltage	0
Output Over-voltage Protection (with respect to feedback voltage)	0
Internal Soft-Start Delay	0
Shutdown threshold	0
Frequency Adjust	0
Line Regulation	0

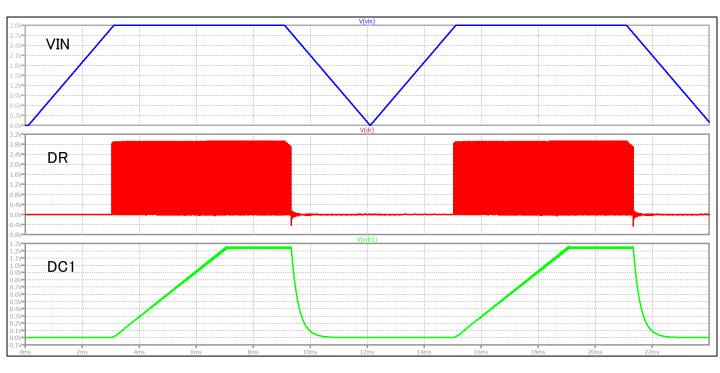


**Testbench for UVLO function** 



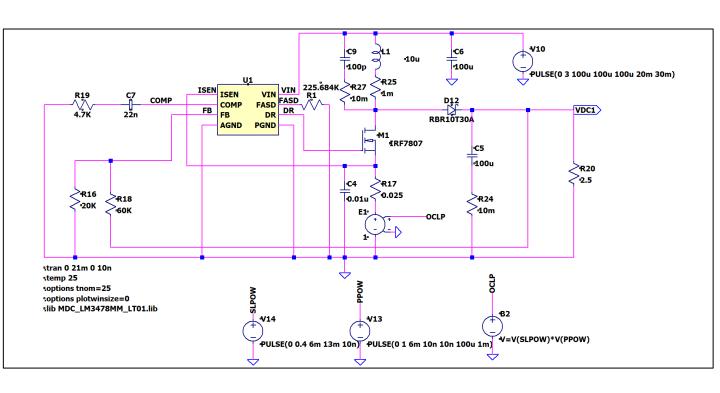


#### **UVLO** function

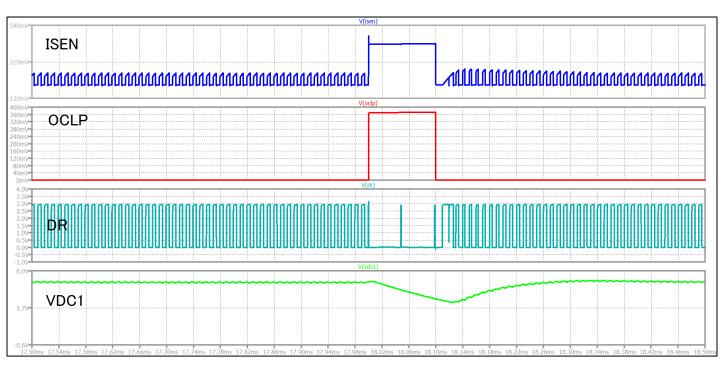




**Testbench for OCP function** 

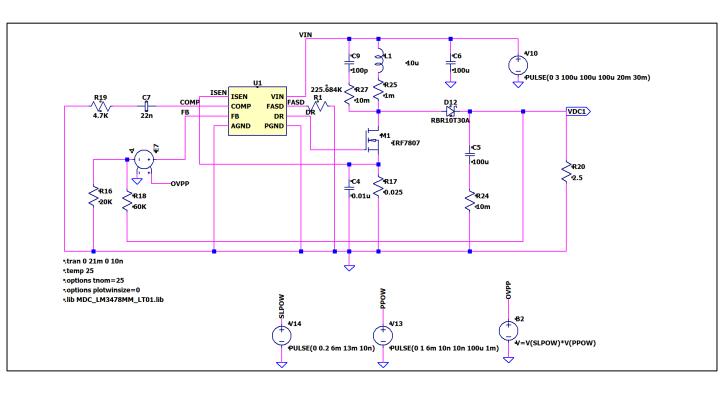






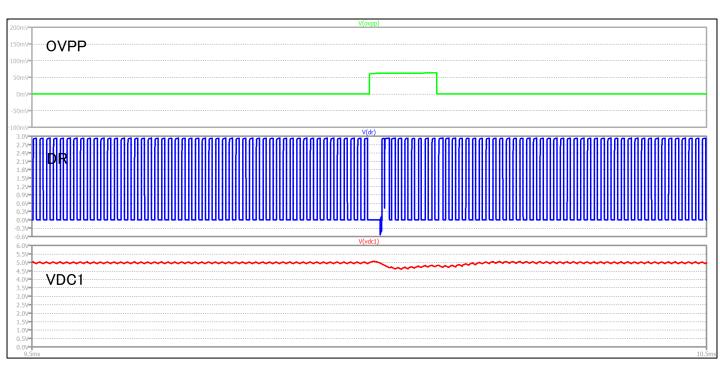


**Testbench for OVP function** 



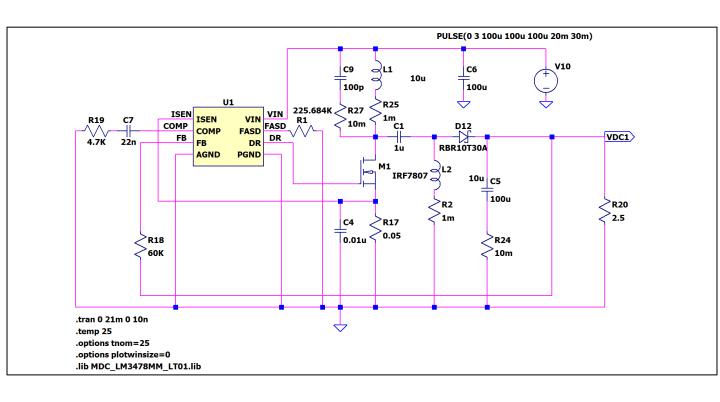


**OVP** function



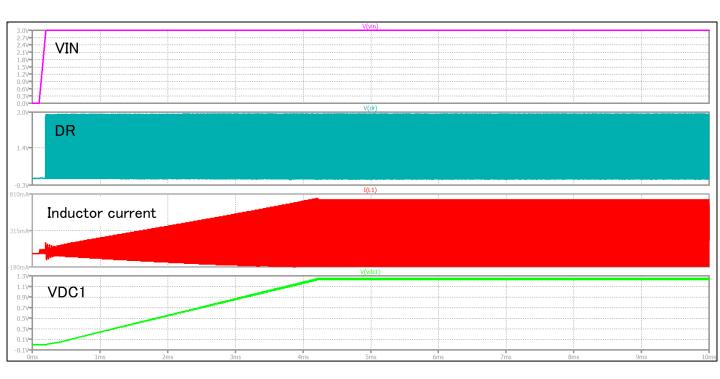


Testbench for Soft start threshold function



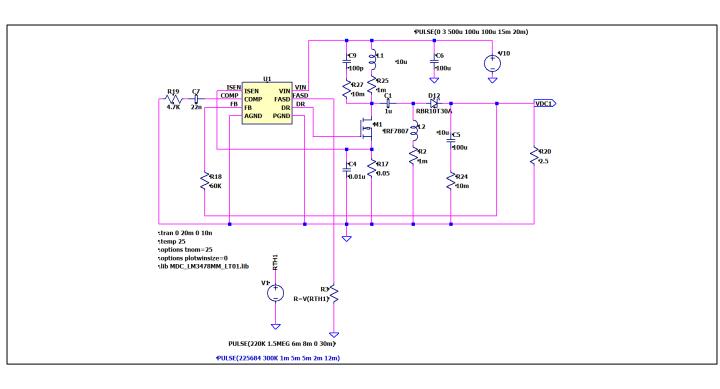


#### Soft start threshold function



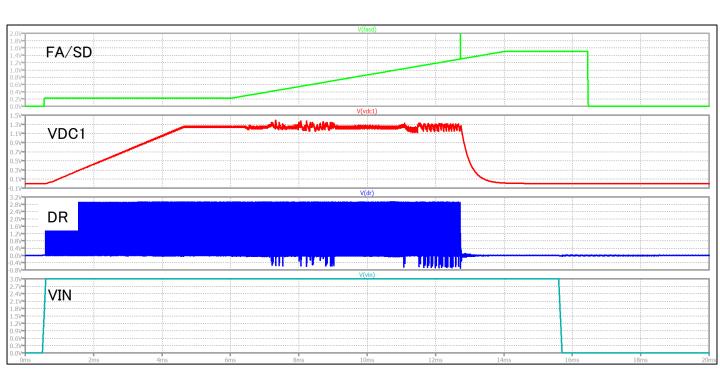


Testbench for Shutdown threshold function



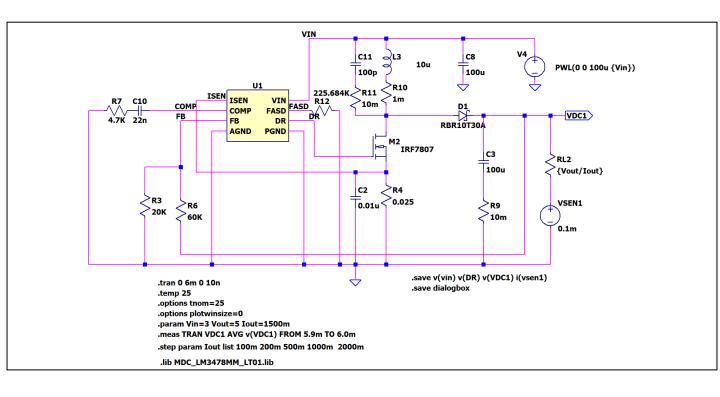


#### Shutdown threshold function



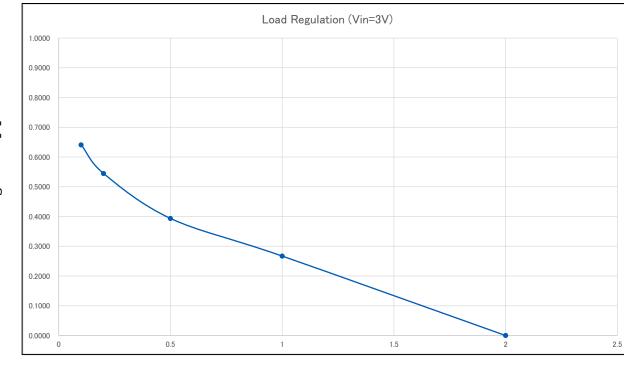


Testbench for load regulation





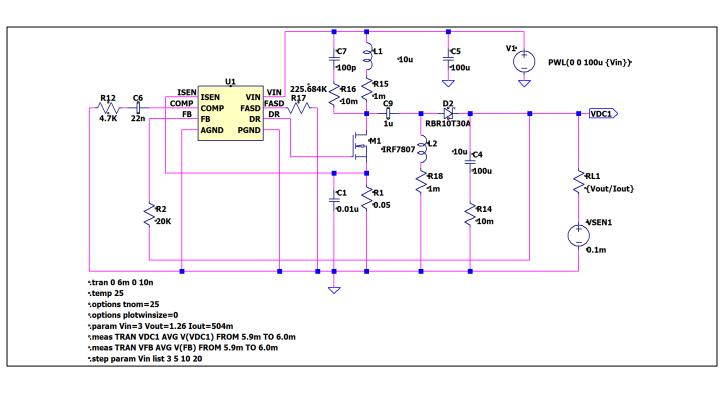
#### Load regulation



Load current [A]

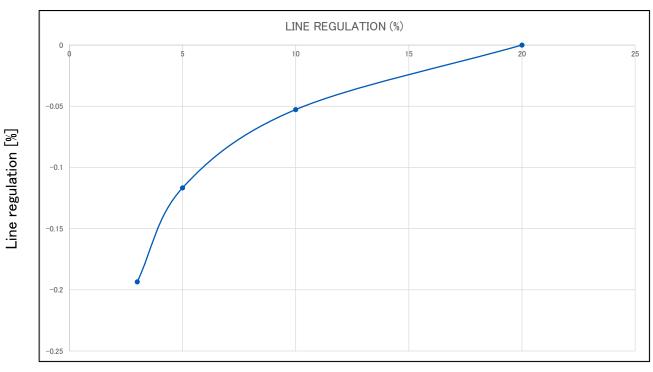


Testbench for line regulation





#### Line regulation



Input Voltage [V]



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