

LTspice Model LDO (Low Drop Out) Infineon TLS850F1TAV50

Model Information

Model An original macro model Call Name MDC_TLS850F1TAV50_LT

Pin Assign 1:1 2:EN 3:RO/WO 4:GND 5:RADJ 6:WI 7:Q

File List Model Library MDC_TLS850F1TAV50_LT01.lib

Model Report MDC_TLS850F1TAV50_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev. 1.0

Product name TLS850F1TAV50

Company name Infineon Technologies AG

● Characteristics VIN-VOUT, Vdrop, Line-Tran, Load-Tran, EN, RESET, WD

Simulation Condition

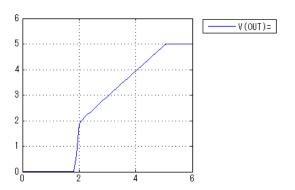
This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



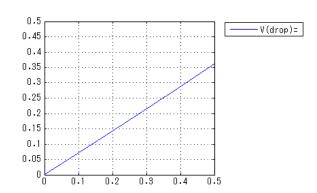
VIN-VOUT

Temp. = 25deg C

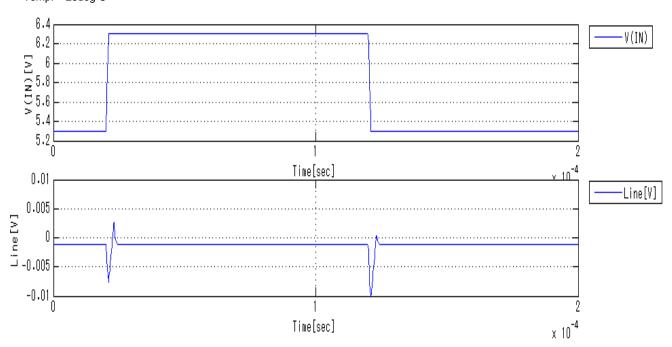


Vdrop

Temp. = 25deg C



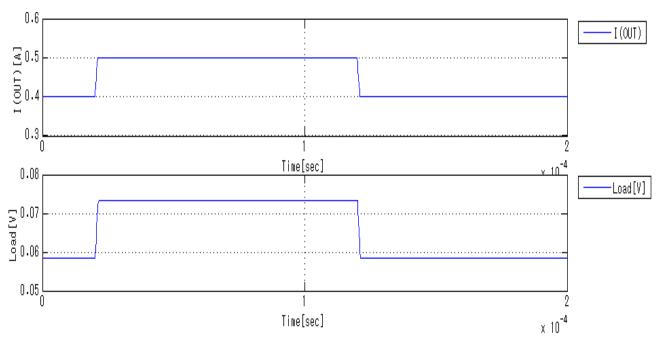
Line-Tran



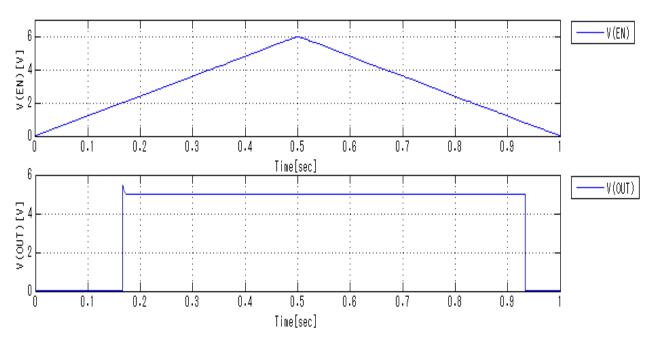


Load-Tran

Temp. = 25deg C

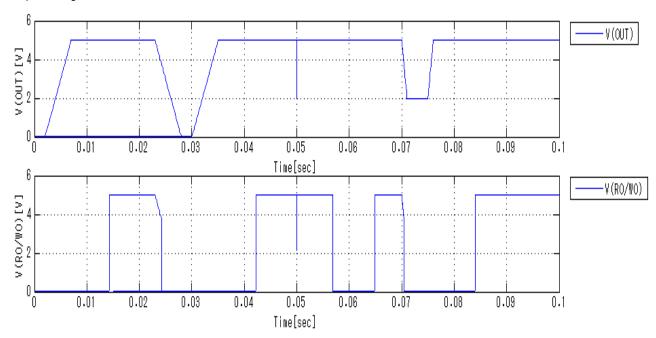


ΕN



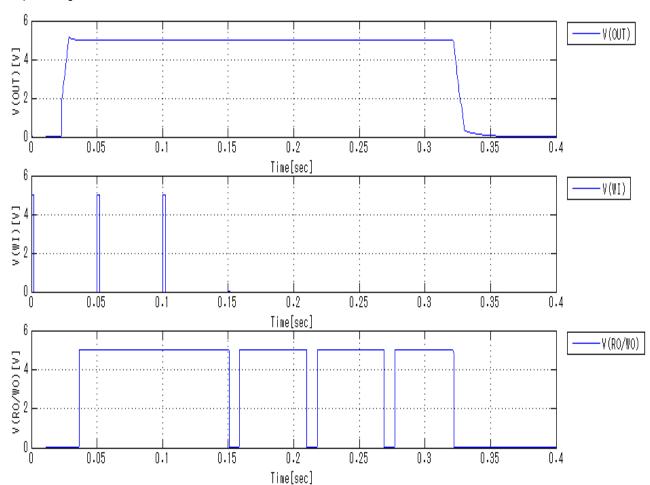


RESET





WD





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MoDeCH Inc.

Head Office

Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/