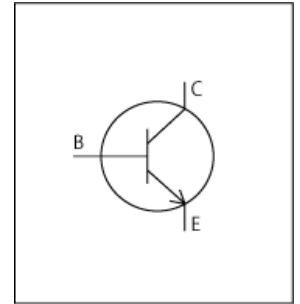


LTspice Model

NPN

ROHM Semiconductor

2SCR553P5T100



Model Information

Model Gummel-Poon model
Call Name MDC_2SCR553P5T100_LT
Pin Assign 1:B 2:C 3:E
File List Model Library MDC_2SCR553P5T100_LT01.lib
 Model Report MDC_2SCR553P5T100_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 20170605 - Rev.002
- Product name 2SCR553P5T100
- Company name ROHM Co., Ltd.
- Characteristics $I_c V_{be}[Temp]$, $I_c V_{ce}[ib]$, $hFE I_c[Temp]$, $hFE I_c[V_{ce}]$, $V_{ce}(sat) I_c[Temp]$, $V_{ce}(sat) I_c[hFE]$, $V_{be}(sat) I_c[Temp]$, $fT I_c[V_{ce}]$, C_{ob} , C_{ib}

Simulation Range

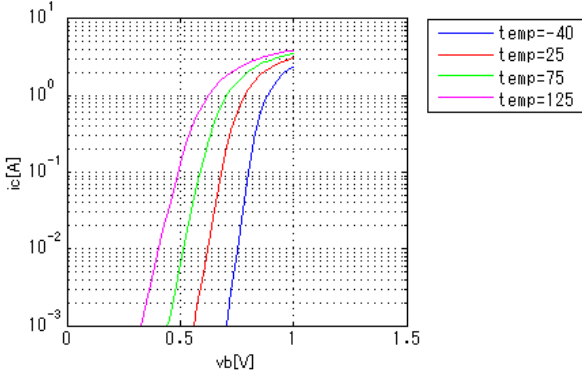
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	50	V
Base current (DC)	0	to	0.2	A
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

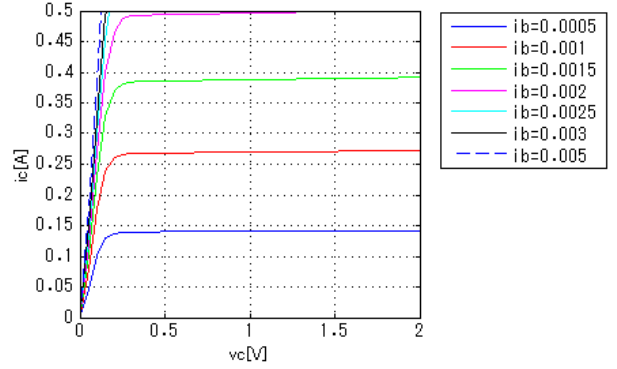
IcVbe[Temp]

Vce = 2V



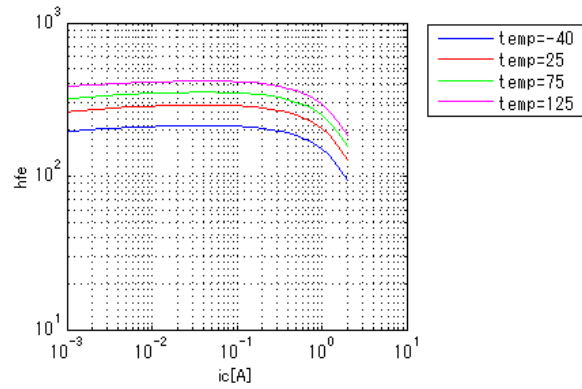
IcVce[ib]

Temp. = 25deg C

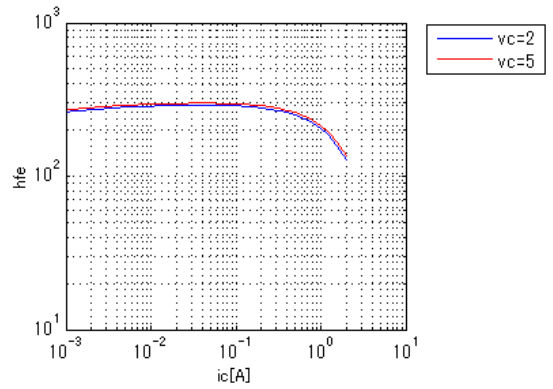


hFEIc[Temp]

Vce = 2V

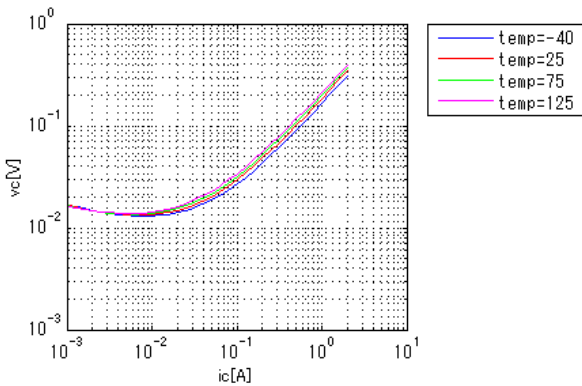


hFEIc[Vce]

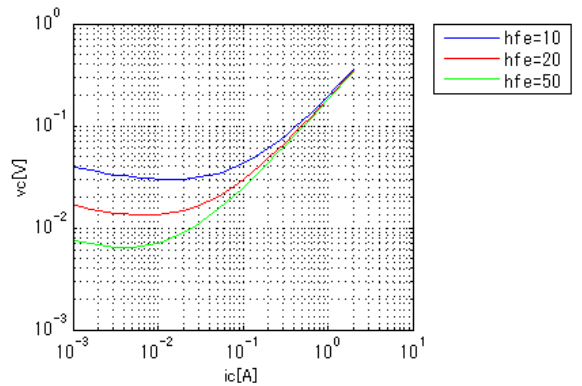


Vce(sat)Ic[Temp]

IC/IB = 20



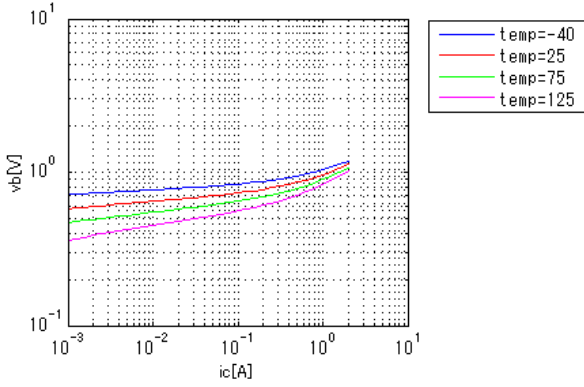
Vce(sat)Ic[hFE]



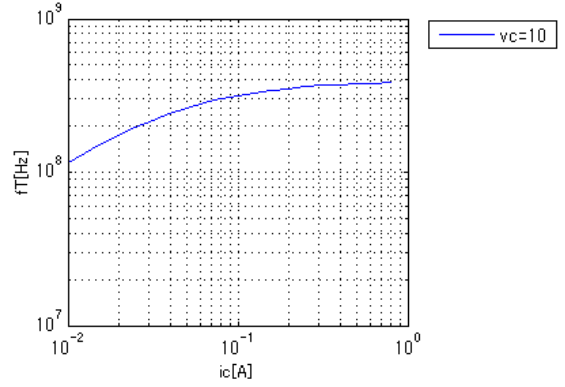
Simulation results are following.
 Explanatory notes — : simulated

Vbe(sat)Ic[Temp]

IC/IB = 20

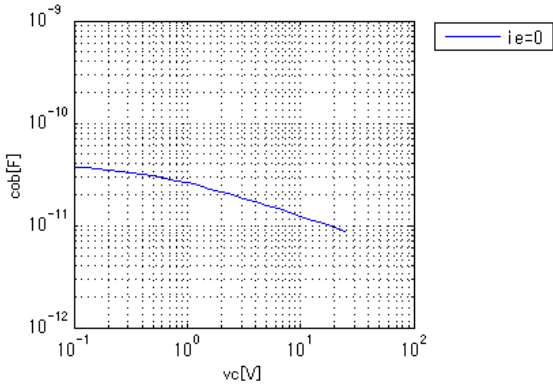


fT[Ic[Vce]]



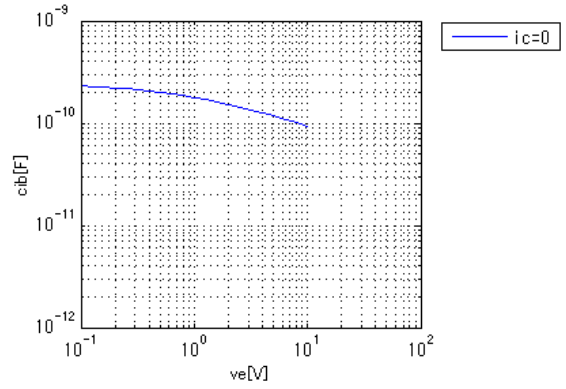
Cob

Freq. = 1MHz



Cib

Freq. = 1MHz



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