

# LTspice Model High-side Driver STMicroelectronics VN7040ASTR

# **Model Information**

Model A macro model Call Name VN7040ASTR

Pin Assign 1:INPUT 2:SEn 3:GND 4:MultiSense 5:VCC 6:OUTPUT 7:OUTPUT 8:VCC

File List Model Library MDC\_VN7040ASTR\_LT.lib

Model Report MDC\_VN7040ASTR\_LT.pdf(this file)

Verified Simulator Version LTspice version XVII

Note

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version 04-Apr-2019Product name VN7040ASTR

Company name STMicroelectronics N.V.

[Characteristics listed]

Characteristics UVLO

Overvoltage Clamp Current limitation

#### **Simulation Condition**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C



#### **Model Functions Table**

Functions	Implemented
Undervoltage shutdown	0
Overvoltage clamp(46V)	0
Load current limitation(34A)	0
Self limiting of fast thermal transients	<b>※</b> 1
MultiSense diagnostic functions	<b>※</b> 2
Sense enable/disable	0

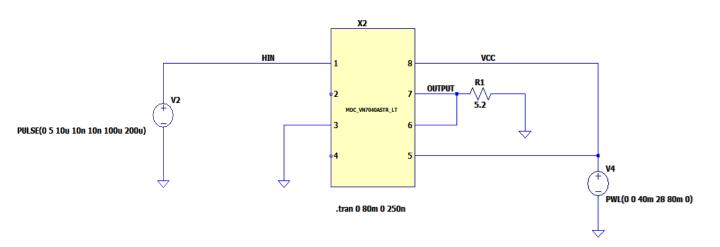
<sup>¾1 The temperature coefficient is unknown, shutdown is performed after a certain time has elapsed after reaching the current limit.</sup> 

<sup>※2</sup> Only for overcurrent shutdown.



# **UVLO Testbench**

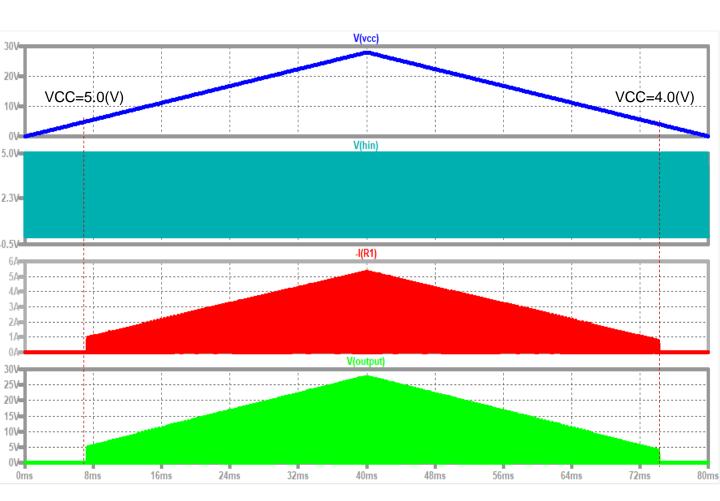
# **Referred to Data Sheet**





Simulation results are following. Explanatory notes — : simulated

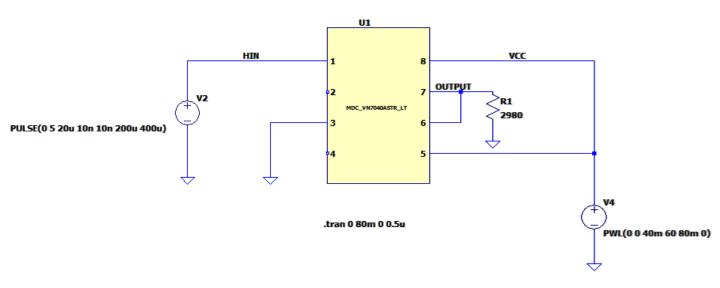
**UVLO** 





# **Overvoltage Clamp Testbench**

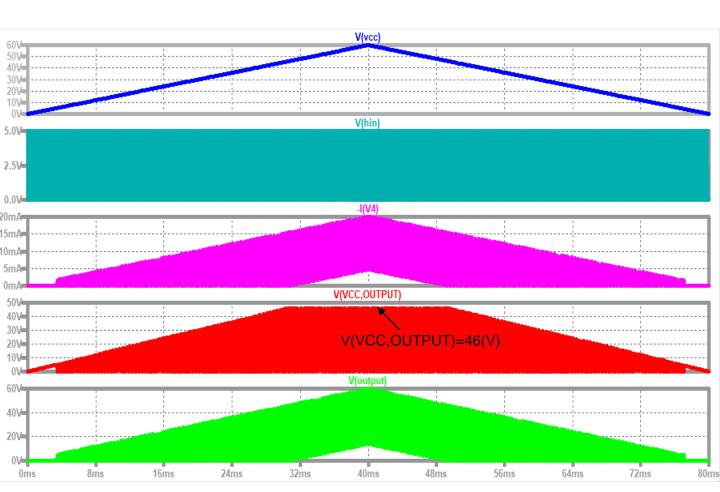
# **Referred to Data Sheet**





Simulation results are following. Explanatory notes — : simulated

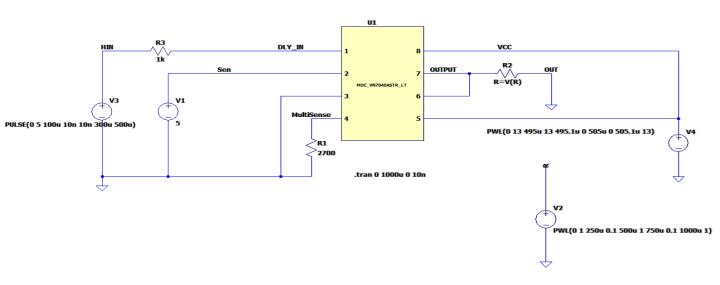
# **Overvoltage Clamp**





# **Current limitation Testbench**

# **Referred to Data Sheet**

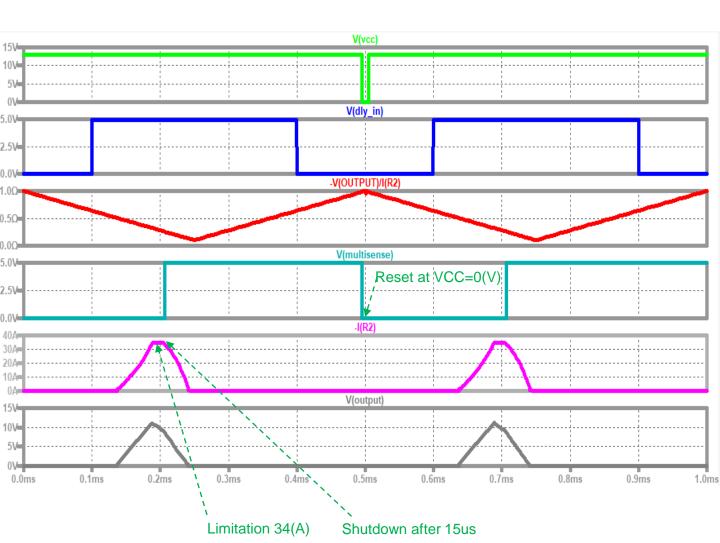


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Simulation results are following. Explanatory notes — : simulated

#### **Current limitation**





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