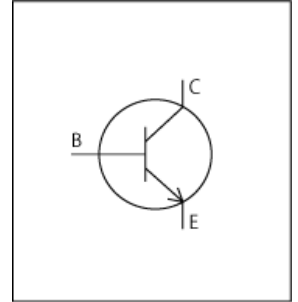


LTspice Model

NPN

JCET

2SD667



Model Information

Model Mextram model
Call Name MDC_2SD667_LT
Pin Assign 1:C 2:B 3:E
File List Model Library MDC_2SD667_LT01.lib
 Model Report MDC_2SD667_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version D,Mar,2016
- Product name 2SD667
- Company name Jiangsu Changjiang Electronics Technology Co., Ltd.
- Characteristics $I_{c[Vce]}$, $h_{FE}I_{c[Temp]}$, $V_{be(sat)}I_{c[Temp]}$, $V_{ce(sat)}I_{c[Temp]}$, f_T , $I_{c[Vce]}$, C_{ob} , C_{ib} , $I_{cVbe[Temp]}$

Simulation Range

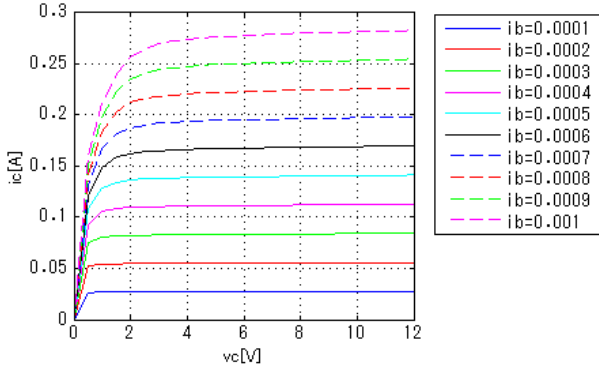
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	80	V
Base current (DC)	0	to	0.1	A
Temperature	-55	to	150	deg C

Simulation results are following.
 Explanatory notes — : simulated

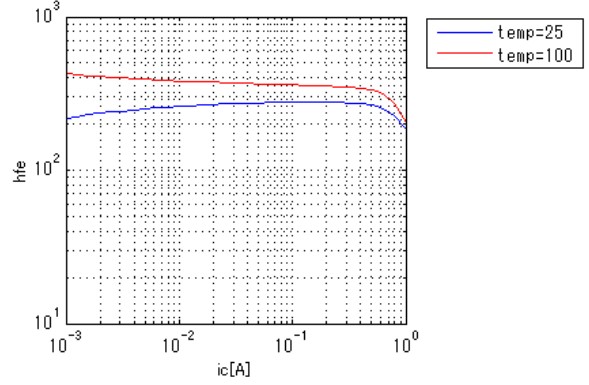
IcVce[ib]

Temp. = 25deg C



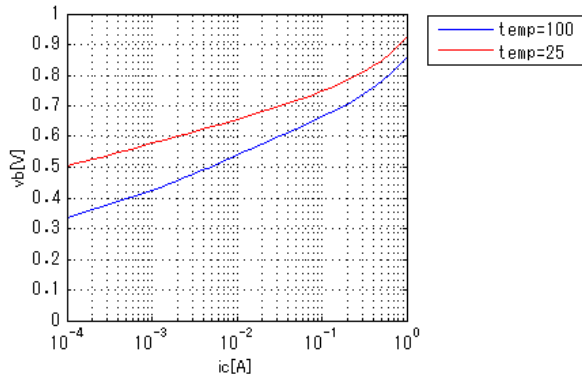
hFEIc[Temp]

Vce = 5V



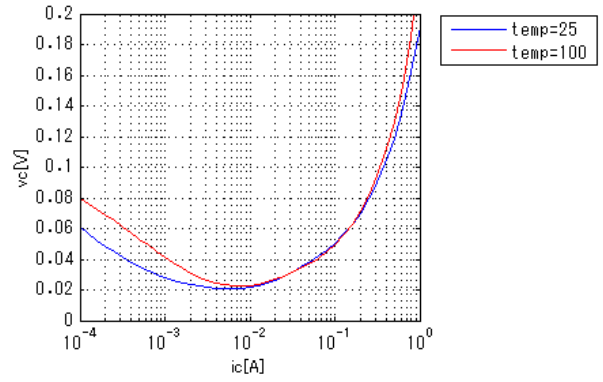
Vbe(sat)Ic[Temp]

IC/IB = 10

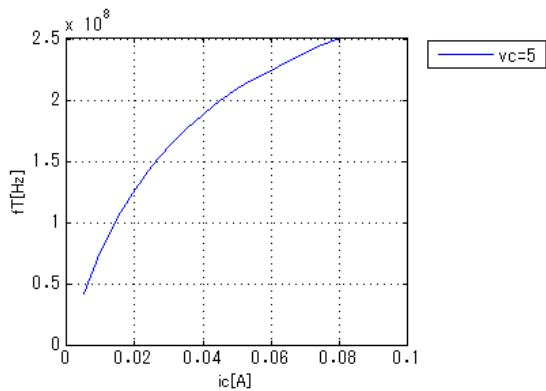


Vce(sat)Ic[Temp]

IC/IB = 10

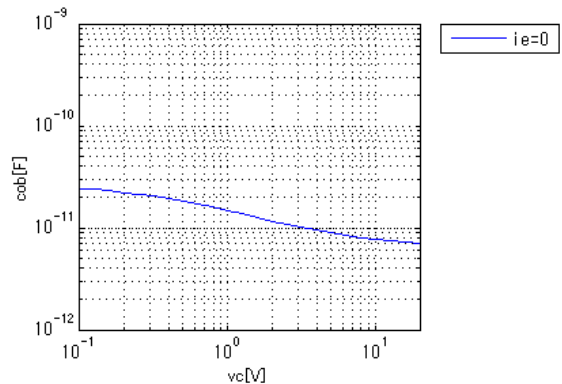


fT[Ic[Vce]]



Cob

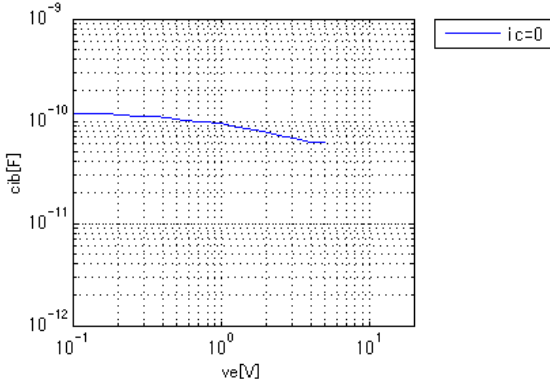
Freq. = 1MHz



Simulation results are following.
 Explanatory notes — : simulated

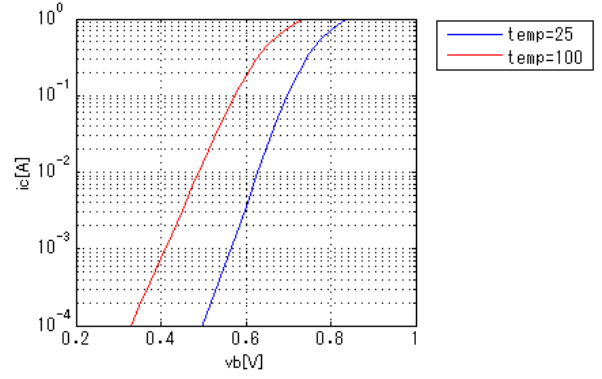
Cib

Freq. = 1MHz



IcVbe[Temp]

$V_{ce} = 5V$



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