

LTspice Model Multilayer Inductor MURATA DFE252012PD-4R7M



Model Information

An original macro model

Call Name MDC DFE252012PD-4R7M LT

Pin Assian 1:1 2:2

File List Model Library MDC_DFE252012PD-4R7M_LT01.lib

> Model Report MDC_DFE252012PD-4R7M_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

Company name

[Data Sheet]

Date/Version 2023/09/30

Product name DFE252012PD-4R7M

Murata Manufacturing Co., Ltd. Characteristics InductanceIf[Temp],InductanceFreq[Vf],ImpedanceFreq[Vf],

QfactorFreq[Vf],ResistanceVf[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Temperature	-40	to	125	deg C



Model Functions Table

Inductor

O: Implemented

×: Not Implemented

—: Not applicable

RANK=1

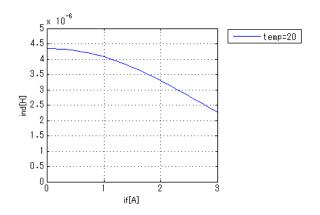
Functions	RANK	Implemented
Impedance-Frequency	1	0
Inductance-Frequency	1	0
Inductance-Current	1	0
Inductance-Temp	1	_
Resistance-Voltage	1	0
Qfactor-Frequency	1	0



Simulation results are following. Explanatory notes — : simulated

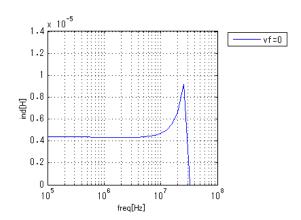
Inductancelf[Temp]

freq = 1000000Hz



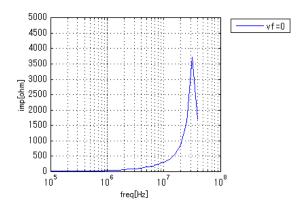
InductanceFreq[Vf]

temp = 20degC



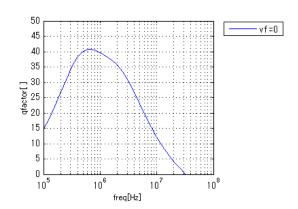
ImpedanceFreq[Vf]

temp = 20degC

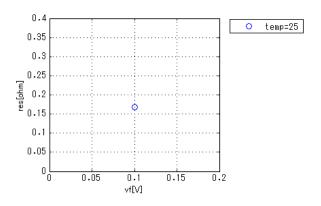


QfactorFreq[Vf]

Temp = 25degC



ResistanceVf[Temp]





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