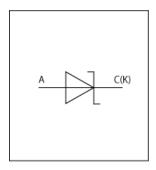


# PSpice Model Zener Diode VISHAY SML4760HE3



## **Model Information**

ModelA macro model based on general SPICE diode modelCall NameMDC\_SML4760HE3\_PSPin Assign1:A 2:CFile ListModel LibraryMDC\_SML4760HE3\_PS01.libModel ReportMDC\_SML4760HE3\_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet] • Date/Version • Product name • Company name

Characteristics

08-Apr-15 SML4760HE3 Vishay Intertechnology, Inc. IfVf[Temp],IrVr[Temp],IrVr(Breakdown)

#### **Simulation Range**

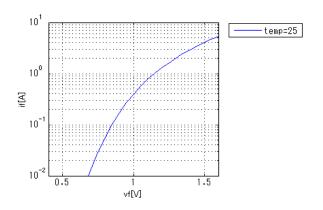
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Forward Voltage	0	to	16	V
Reverse Voltage	0	to	136	V
Temperature	-65	to	150	deg C

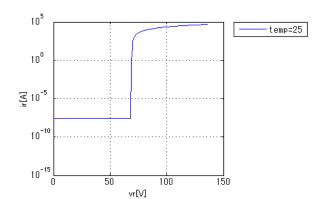


# Simulation results are following. Explanatory notes -: simulated

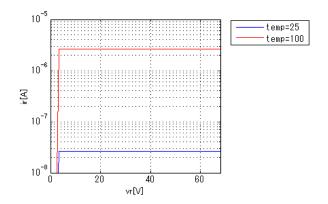
### lfVf[Temp]



### IrVr(Breakdown)



### IrVr[Temp]





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