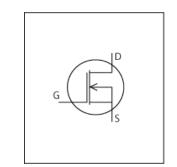


LTspice Model NMOS Infineon BSC039N06NS



Model Information

Model A macro model based on BSIM3 model

Call Name MDC_BSC039N06NS_LT

Pin Assign 1:S 2:S 3:S 4:G 5:D 6:D 7:D 8:D

File List Model Library MDC_BSC039N06NS_LT02.lib

Model Report MDC_BSC039N06NS_LT.pdf (this file)

Verified Simulator Version

Note

LTspice version XVII

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameRev. 2.4, 2020-02-03BSC039N06NS

●Company name Infineon Technologies AG

● Characteristics IdVds[Vgs],Rds(on)Id[Vgs],IdVgs[Temp],YfsId[Temp],Rds(on

)Temp[Id],VthTemp[Id],CapacitanceVds[Cname],IsVsd[Temp],VgsQg[Vdd],BvTemp[ir],SwitchingIdd[Tname],SwitchingWa

veform,Trrlf[Ir],Qrrlf[Ir],TrrQrrWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

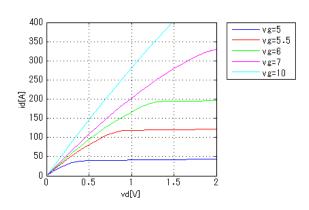
Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	60	V
Gate-source voltage (DC)	-20	to	20	V
Temperature	-55	to	150	deg C



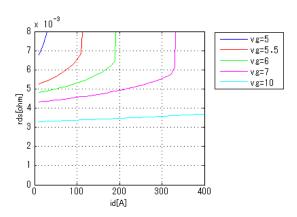
Simulation results are following. Explanatory notes — : simulated

IdVds[Vgs]

Temp. = 25degC

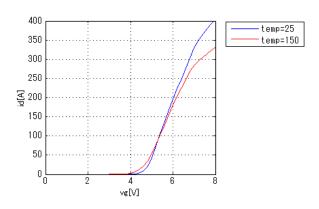


Rds(on)Id[Vgs]



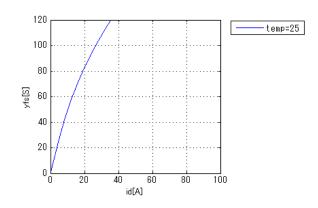
IdVgs[Temp]

Vds = 2V



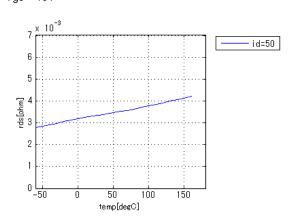
Yfsld[Temp]

Vds = 2V



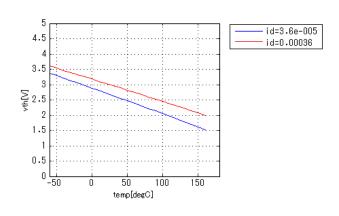
Rds(on)Temp[Id]

Vgs = 10V



VthTemp[ld]

Vd = Vg

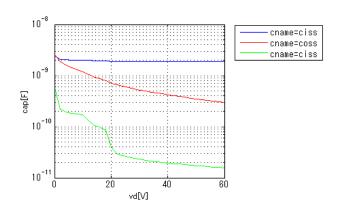




Simulation results are following. Explanatory notes — : simulated

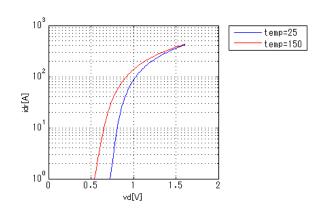
CapacitanceVds[Cname]

freq = 1000000Hz



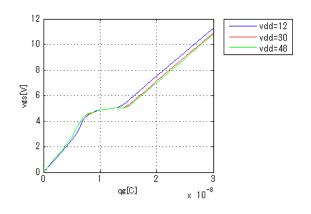
IsVsd[Temp]

vg = 0V

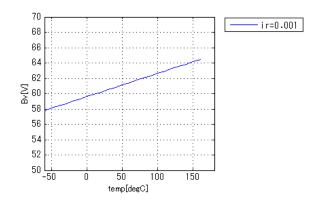


VgsQg[Vdd]

Id = 50A

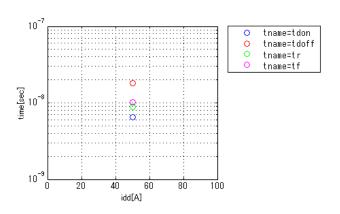


BvTemp[ir]



Switchingldd[Tname]

vgg = 10V, vdd = 30V, RGG = 3.0ohm

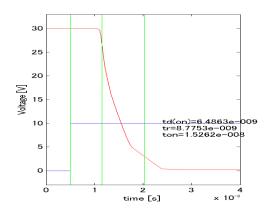


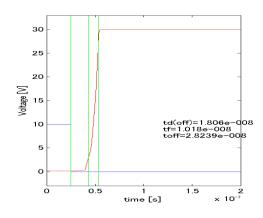


Simulation results are following. Explanatory notes — : simulated

SwitchingWaveform

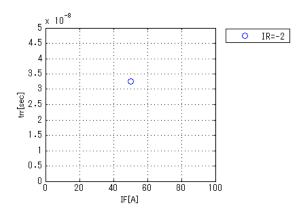
Blue: INPUT Red: OUTPUT





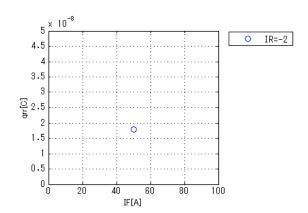
Trrlf[lr]

vdd = 30V, didt = 100A/us

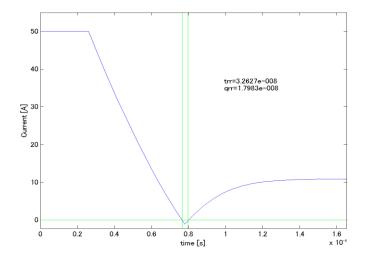


Qrrlf[lr]

vdd = 30V, didt = 100A/us



TrrQrrWaveform





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