

LTspice Model ESD Protection Diode VISHAY VTVS36ASMF-HM3

Model Information

 Model
 A macro model based on general SPICE diode model

 Call Name
 MDC_VTVS36ASMF-HM3_LT

 Pin Assign
 1:A 2:C

 File List
 Model Library
 MDC_VTVS36ASMF-HM3_LT01.lib

 Model Report
 MDC_VTVS36ASMF-HM3_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

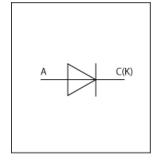
The information which was used for modeling is as follow:

15-Dec-15 VTVS36ASMF-HM3 Vishay Intertechnology, Inc. IrVr(Breakdown)

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

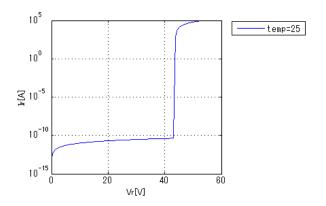
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	86	V
Temperature	-55	to	175	deg C





Simulation results are following. Explanatory notes -: simulated

IrVr(Breakdown)





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