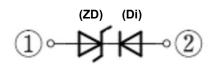


LTspice Model Power Clamper SHINDENGEN ST03D-82



Model Information

| Model | A macro model based on general SPICE diode model | | | | |
|----------------------------|--------------------------------------------------|--------------------------------|--|--|--|
| Call Name | MDC_ST03D-82_LT | | | | |
| Pin Assign | 1:1 2:2 | | | | |
| File List | Model Library | MDC_ST03D-82_LT.lib | | | |
| | Model Report | MDC_ST03D-82_LT.pdf(this file) | | | |
| Verified Simulator Version | | LTspice XVII | | | |

Verified Simulator Version

Note

References

The information which was used for modeling is as follow:

[Data Sheet]

| Date/Version Product name Company name | Rev.01(2020.01) ST03D-82 Shindengen Electric Manufacturing Co., Ltd. |
|------------------------------------------------------------------------------|----------------------------------------------------------------------------|
| [Characteristics listed] | BvTemp, IrTemp(ZD), IrTemp(Di), CjFreq[VR](ZD), |
| ●Characteristics | CjFreq[VR](Di), CjVr(ZD), CjVf(Di), |

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | Unit | |
|-------------|-------|----|------|-------|
| | Min. | | Max. | |
| Temperature | 0 | to | 150 | deg C |

Modech

MDC_ST03D-82_LT

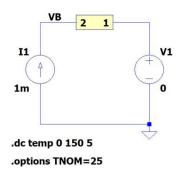
| Diode | | O : Implemented \times : Not Implemented — : Not applicable | |
|-----------------------|--------|---------------------------------------------------------------------|--|
| Model Functions Table | RANK=1 | | |
| Functions | RANK | Implemented | |
| BV-Temp | 1 | 0 | |
| IR-Temp | 1 | 0 | |
| Capacitance-Frequency | 1 | 0 | |
| Capacitance-VR | 1 | 0 | |



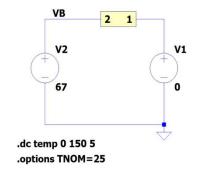
BvTemp, IrTemp(ZD) Testbench

Referred to Data Sheet

BvTemp

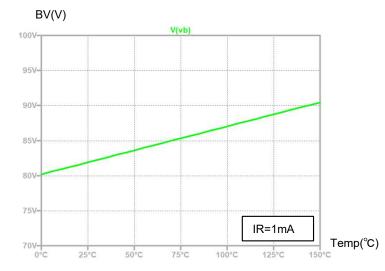


IrTemp(ZD)

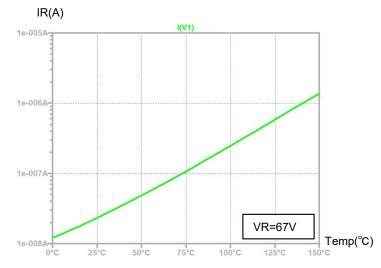


Simulation results are following. Explanatory notes — : simulated

BvTemp



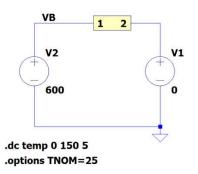
IrTemp(ZD)

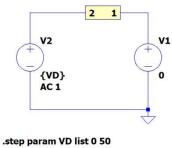




IrTemp(Di), CjFreq[VR](ZD) Testbench Referred to Data Sheet

IrTemp(Di)



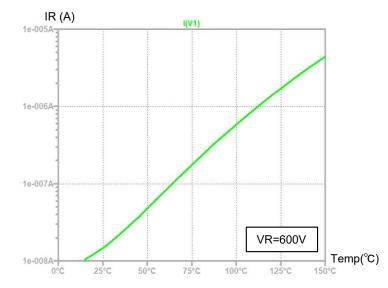


.ac dec 10 1000 1000000 .options TNOM=25

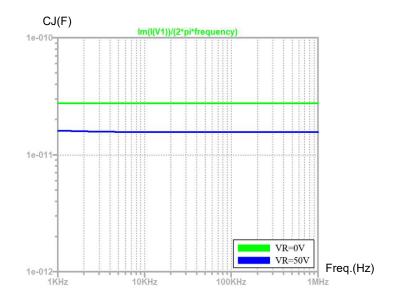
CjFreq[VR](ZD)

Simulation results are following. Explanatory notes — : simulated

IrTemp(Di)



CjFreq[VR](ZD)

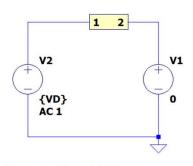




CjFreq[VR](Di), CjVr(ZD) Testbench

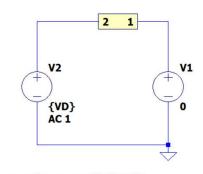
Referred to Data Sheet

CjFreq[VR](Di)



.step param VD list 0 50 .ac dec 10 1000 1000000 .options TNOM=25

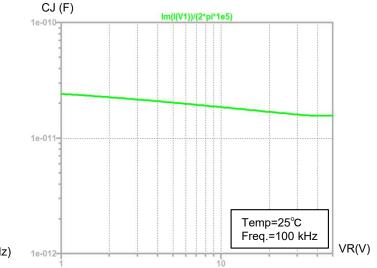
CjVr(ZD)



.step dec param VD 1 50 10 .ac lin 1 100000 100000 .options TNOM=25

Simulation results are following. Explanatory notes - : simulated

CjFreq[VR](Di)



CJ (F) lm(I(V1))/(2*pi*frequency) 1e-010 1e-011-VR=0V VR=50V 1MHz Freq.(Hz) 1e-012 1KHz

100KHz

CjVr(ZD)

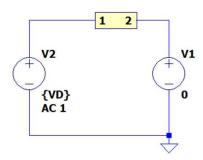
10KHz



CjVr, trrlflr Testbench

Referred to Data Sheet

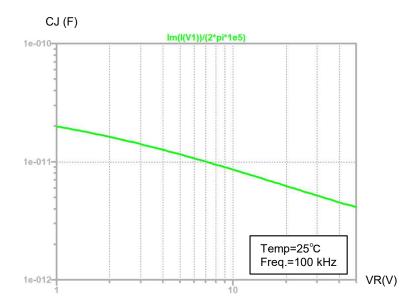
CjVr(Di)



[.]step dec param VD 1 50 10 .ac lin 1 100000 100000 .options TNOM=25

Simulation results are following. Explanatory notes — : simulated

CjVr(Di)





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MoDeCH Inc.

Head Office Location: 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/