

MDC_2STF1360_PS

PSpice Model NPN STM 2STF1360

Model Information

ModelMEXTRAMCall NameMDC_2STF1360_PSPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_2STF1360_PS02.lib MDC_2STF1360_PS.pdf (this file)

Verified Simulator Version Note

PSpice version 16.6

References

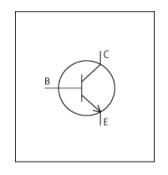
The information which was used for modeling is as follow:

August 2012 Rev 3 2STF1360 STMicroelectronics NV hFEIc[Temp],Vce(sat)Ic[Temp],Vbe(sat)Ic[Temp],Cob,Cib

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|--------------------------------|-------|----|------|-------|
| | Min. | | Max. | |
| Collector-emitter voltage (DC) | 0 | to | 60 | V |
| Base current (DC) | 0 | to | 0.2 | А |
| Temperature | -65 | to | 150 | deg C |

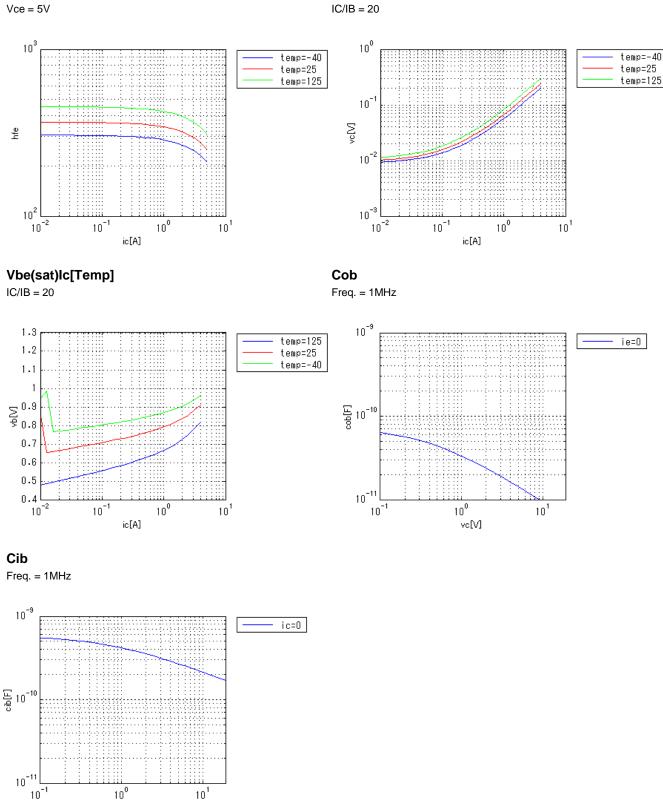




Simulation results are following. Explanatory notes - : simulated



Vce = 5V



Vce(sat)lc[Temp]

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ve[V]



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MoDeCH Inc.

Head Office Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan Tel:+81-42-656-3360 E-Mail:model-on-support@modech.co.jp URL:http://www.modech.com/en/