

## LTspice Model Varistor Ropla CNR-10D431K

## **Model Information**

 Model
 A macro model

 Call Name
 MDC\_CNR-10D431K\_LT

 Pin Assign
 1:1 2:2

 File List
 Model Library
 MDC\_CNR-10D431K\_LT01.lib

 Model Report
 MDC\_CNR-10D431K\_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

#### References

The information which was used for modeling is as follow:

17/03/07 CNR-10D431K Ropla Elektronik Sp.Z.o.o IrVr[Temp],BvTemp,IrVr(Breakdown)

#### **Simulation Range**

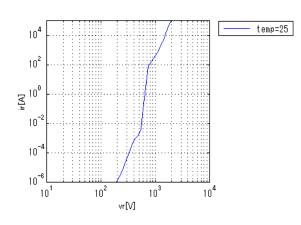
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	700	V
Temperature	-40	to	125	deg C

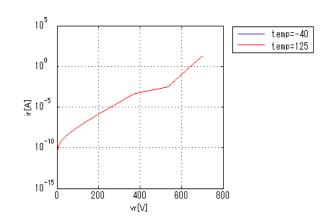


# Simulation results are following. Explanatory notes -: simulated

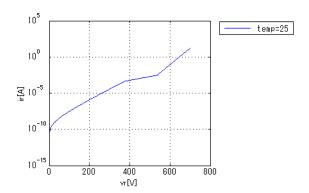
### IrVr[Temp]



lrvr\_brd



lrvr\_typ





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