

LTspice Model Transistor Array TI ULN2003APWR

Model Information

Model An original macro model
Call Name MDC_ULN2003APWR_LT
Pin Assign 1:1B 2:2B 3:3B 4:4B 5:5B 6:6B 7:7B 8:E 9:COM 10:7C 11:6C 12:5C 13:4C 14:3C 15:2C 16:1C
File List Model Library MDC_ULN2003APWR_LT01.lib
 Model Report MDC_ULN2003APWR_LT.pdf (this file)

Verified Simulator Version LTspice version XVII
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2001.11
- Product name ULN2003APWR
- Company name Texas Instruments Inc.
- Characteristics VCEICIB
Transient

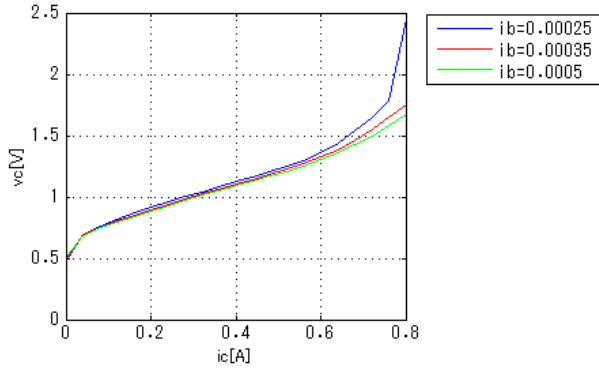
Simulation Condition

This table shows the value of evaluated simulation condition that was not occurs any convergence problems in this area.

Item	Condition	Unit
Temperature	25	deg C

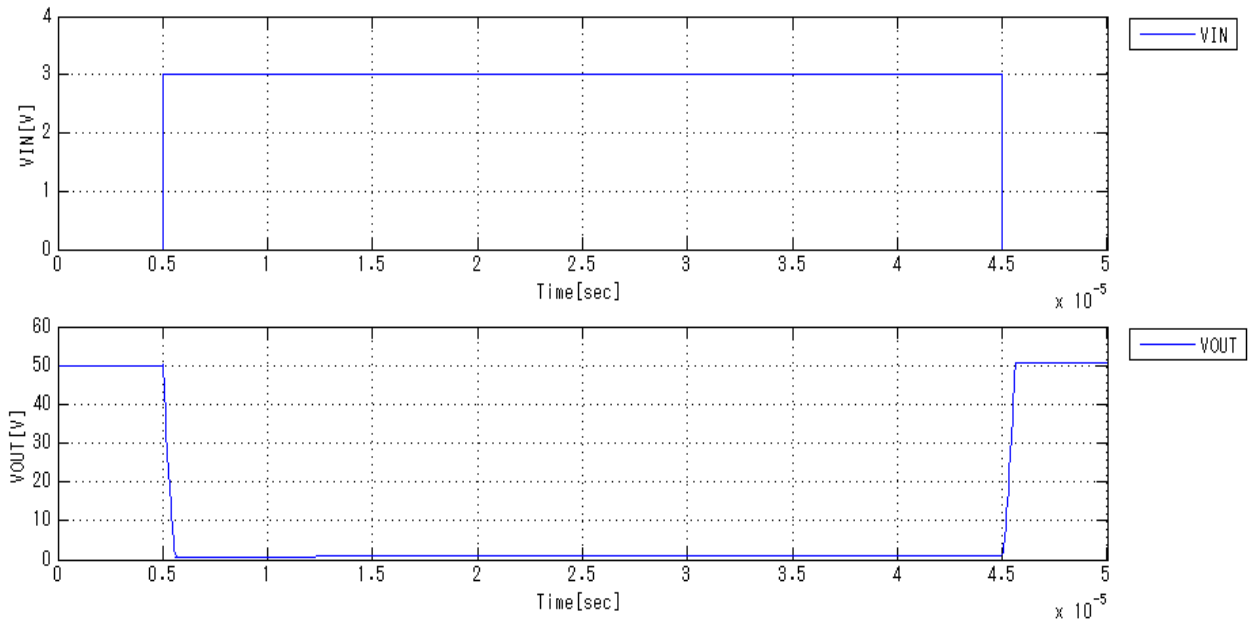
Simulation results are following.
Explanatory notes — : simulated

VCEICIB



Transient

Temp. = 25deg C



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