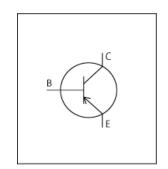


# PSpice Model PNP ON NSS1C300ET4G



## **Model Information**

ModelGummel-Poon modelCall NameMDC\_NSS1C300ET4G\_PS

Pin Assign 1:C 2:B 3:E

File List Model Library MDC\_NSS1C300ET4G\_PS02.lib

Model Report MDC\_NSS1C300ET4G\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Rev.7

Product nameCompany nameNSS1C300ET4GON Semiconductor.

● Characteristics hFEIc[Temp], Vce(sat)Ic[Temp], Vbe(sat)Ic[Temp], IcVbe[Temp]

p],Vcelb[Ic],Cib,Cob,fTIc[Vce]

#### **Simulation Range**

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

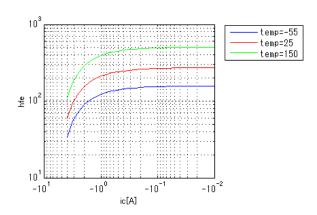
| Item                           | Range |    |       | Unit  |
|--------------------------------|-------|----|-------|-------|
|                                | Min.  |    | Max.  |       |
| Collector-emitter voltage (DC) | 0     | to | 100   | V     |
| Base current (DC)              | 0     | to | -0.30 | Α     |
| Temperature                    | -65   | to | 150   | deg C |



Simulation results are following. Explanatory notes — : simulated

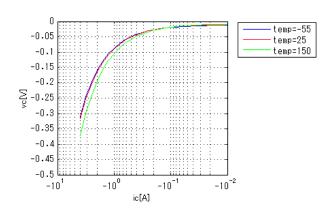
### hFElc[Temp]

Vce = -2V



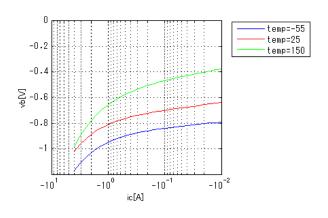
### Vce(sat)lc[Temp]

IC/IB = 10



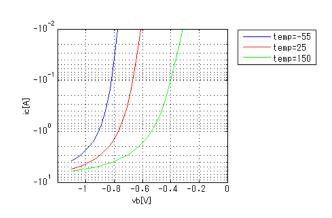
#### Vbe(sat)lc[Temp]

IC/IB = 10



#### IcVbe[Temp]

Vce = -2V

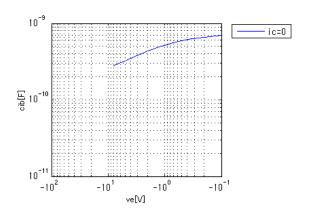


## Vcelb[lc]

0 ic=-0.1 -0.2 ic=-0.5 -0.4 ic=-1 ic=-2 -0.6 ic=-3 -0.8 -1.2 -1.4 -1.6 -1.8 -9  $-10^{-2}$ -10°  $-10^{-1}$  $-10^{-3}$  $-10^{-4}$ іь[А]

#### Cib

Freq. = 1MHz



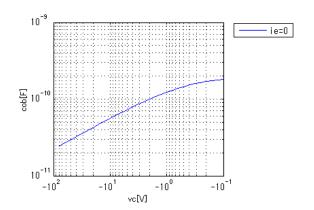


Simulation results are following.

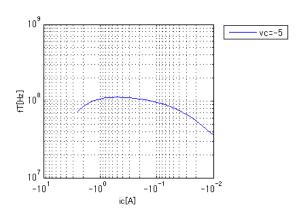
Explanatory notes — : simulated

#### Cob

Freq. = 1MHz



# fTIc[Vce]





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MoDeCH Inc.

**Head Office** 

Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/