

MDC_2SC5209-T111-1J_LT

LTspice Model NPN Isahaya 2SC5209-T111-1J

Model Information

MDC_2SC5209-T111-1J_LT01.lib MDC_2SC5209-T111-1J_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

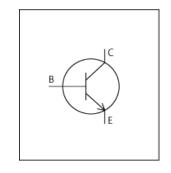
The information which was used for modeling is as follow:

2011.06 2SC5209-T111-1J Isahaya Electronics Corporation IcVce[ib],IcVbe[Temp],hFEIc[Temp],VceIb[Ic],fTIe[Vce],Cob, Vce(sat)Ic[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

| Item | Range | | | Unit |
|--------------------------------|-------|----|------|-------|
| | Min. | | Max. | |
| Collector-emitter voltage (DC) | 0 | to | 50 | V |
| Collector current (DC) | 0 | to | 1 | А |
| Temperature | -55 | to | 150 | deg C |





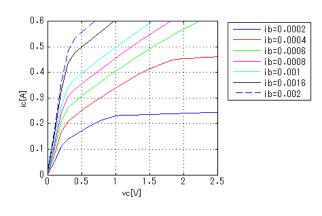
| BJT | | O : Implemented × : Not Implemented | |
|-----------------------|----------------|--|--|
| Model Functions Table | Not applicable | | |
| Functions | RANK | Implemented | |
| IC-VBE(Temp) | 1 | 0 | |
| IC-VCE-IB(Temp) | 1 | 0 | |
| IC-hFE(Temp) | 1 | 0 | |
| VCE(sat)-IC | 1 | 0 | |
| VBE(sat)-IC | 1 | — | |
| Capacitance | 1 | 0 | |
| Transition | 1 | 0 | |
| Switching | 1 | _ | |
| VCE-IB | 1 | 0 | |



Simulation results are following. Explanatory notes — : simulated

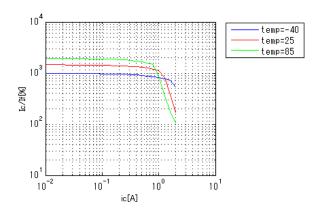
lcVce[ib]

Temp = 25degC



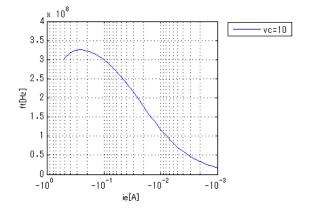
hFElc[Temp]

Vce = 6V





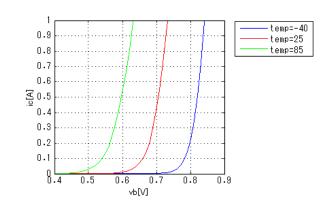
Freq = 5000000Hz



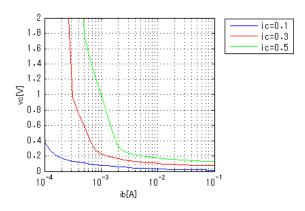
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lcVbe[Temp]

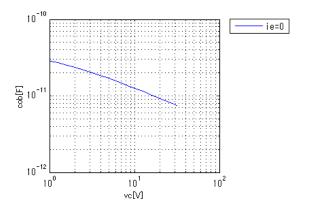
Vce = 6V



Vcelb[lc]



Cob Freq = 100000Hz

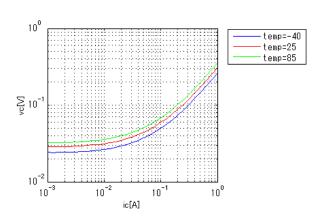




Simulation results are following. Explanatory notes — : simulated

Vce(sat)lc[Temp]







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