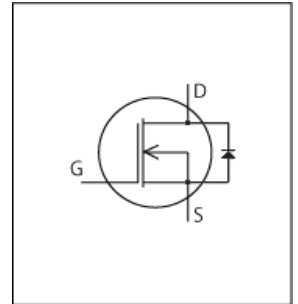


# LTspice Model

## NMOS

### Infineon

### IAUC80N04S6N036



### Model Information

**Model** A macro model based on BSIM3 model  
**Call Name** MDC\_IAUC80N04S6N036\_LT  
**Pin Assign** 1:S 2:S 3:S 4:G 5:D 6:D 7:D 8:D  
**File List** Model Library MDC\_IAUC80N04S6N036\_LT01.lib  
 Model Report MDC\_IAUC80N04S6N036\_LT.pdf (this file)

**Verified Simulator Version** LTspice version XVII  
**Note**

### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version Rev. 1.0 2019-04-01
- Product name IAUC80N04S6N036
- Company name Infineon Technologies AG
- Characteristics IdVds[Vgs],Rds(on)Id[Vgs],IdVgs[Temp],Rds(on)Temp[Id],Vt hTemp[Id],CapacitanceVds[Cname],IsVsd[Temp],BvTemp[ir],VgsQg[Vdd],SwitchingIdd[Tname],Trrlf[Ir],Qrrlf[Ir],Switching Waveform,TrrWaveform

### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	40	V
Gate-source voltage (DC)	-20	to	20	V
Temperature	-55	to	175	deg C

## MOSFET

○ : Implemented  
× : Not Implemented  
— : Not applicable

Model Functions Table

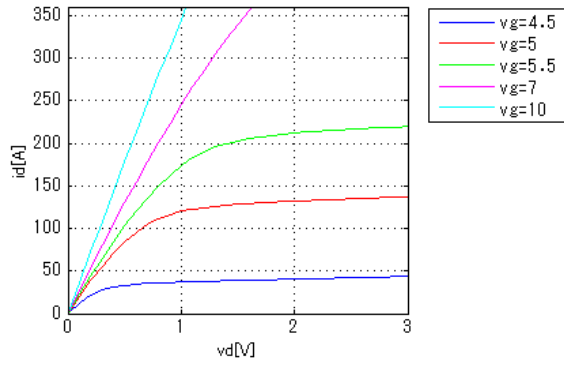
RANK=1

Functions	RANK	Implemented
ID-VDS-VGS	1	○
ID-VGS(Temp)	1	○
RDS(on)	1	○
Capacitance	1	○
Gate Charge	1	○
IS-VSD(Forward)	1	○
Reverse recovery	1	○
Switching(Typ.)	1	○
Bv	1	○
Yfs	1	—
Vth	1	○

Simulation results are following.  
 Explanatory notes — : simulated

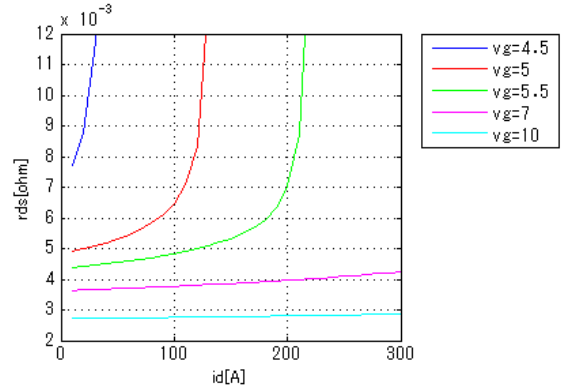
**IdVds[Vgs]**

Temp = 25degC



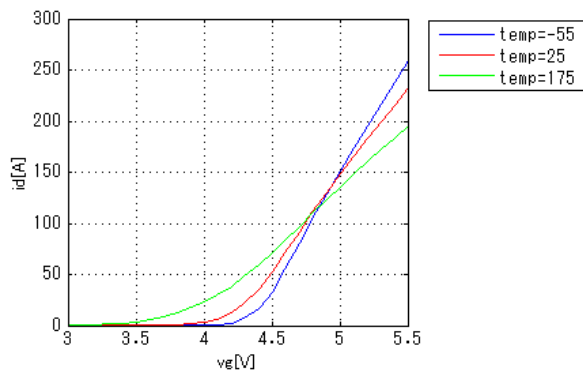
**Rds(on)Id[Vgs]**

Temp = 25degC



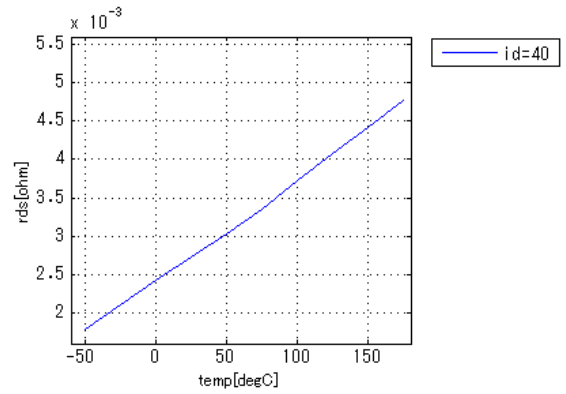
**IdVgs[Temp]**

Vds = 6V



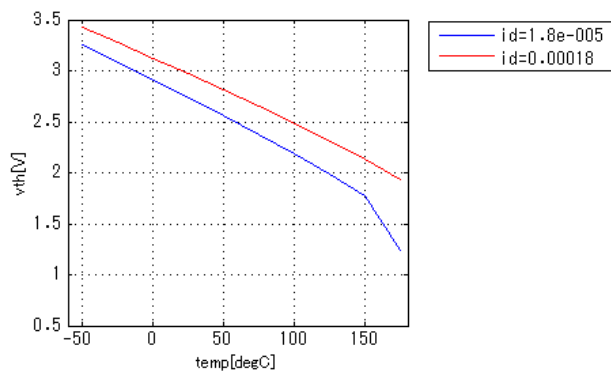
**Rds(on)Temp[Id]**

Vgs = 10V



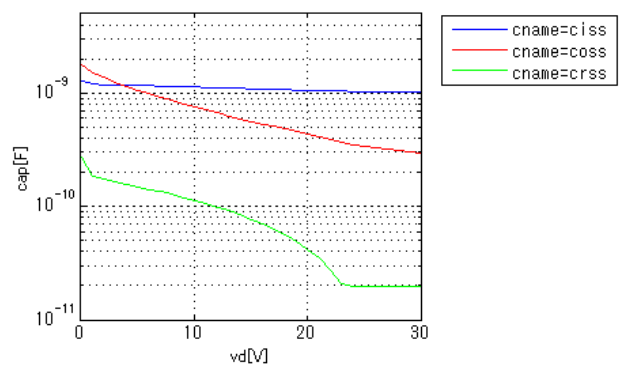
**VthTemp[Id]**

Vd = Vg



**CapacitanceVds[Cname]**

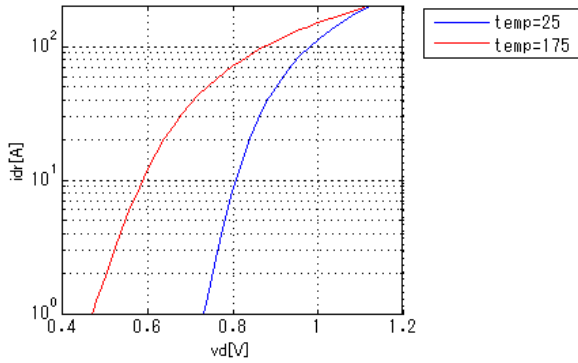
freq = 1000000Hz



Simulation results are following.  
 Explanatory notes — : simulated

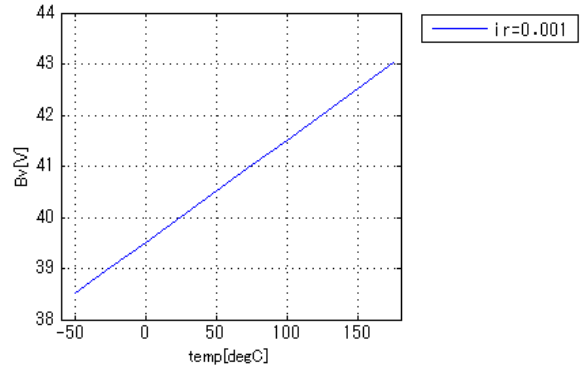
**IsVsd[Temp]**

vg = 0V



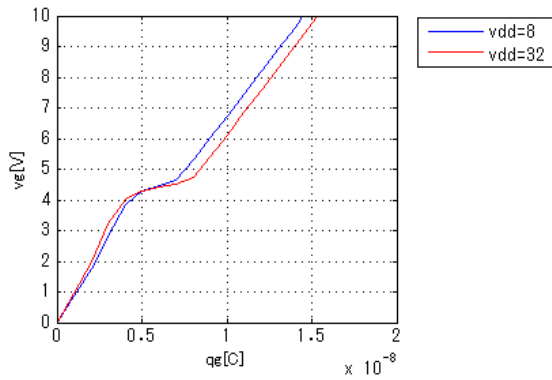
**BvTemp[ir]**

ir = 0.001A



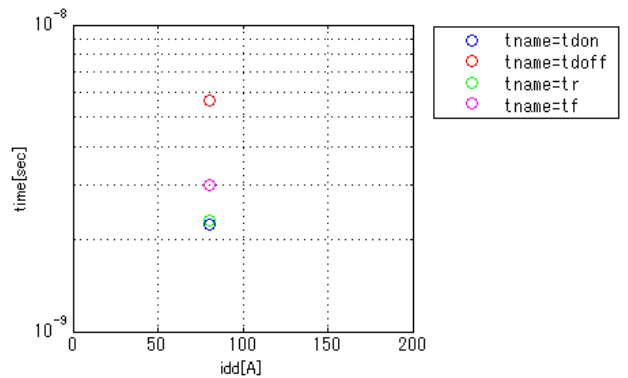
**VgsQg[Vdd]**

Id = 40A



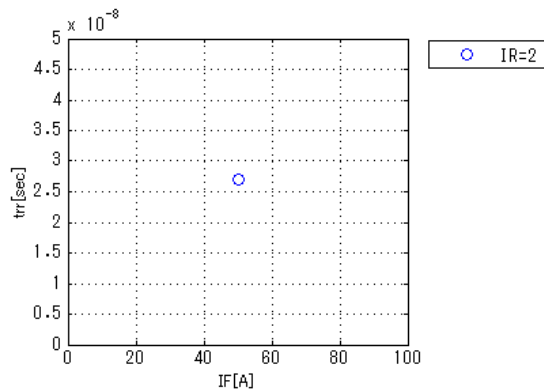
**SwitchingIdd[Tname]**

vgg = 10V, vdd = 20V, RGG = 3.5ohm



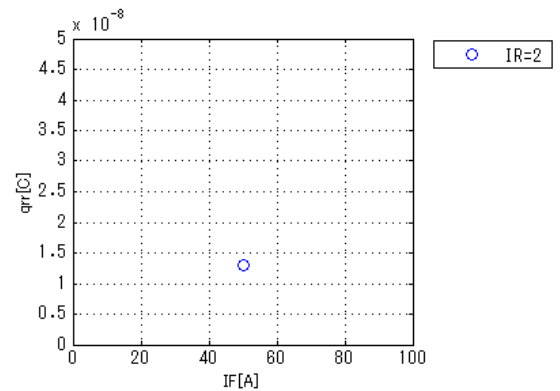
**Trrlf[Ir]**

vdd = 20V, didt = 100A/us, Temp = 25degC



**Qrrlf[Ir]**

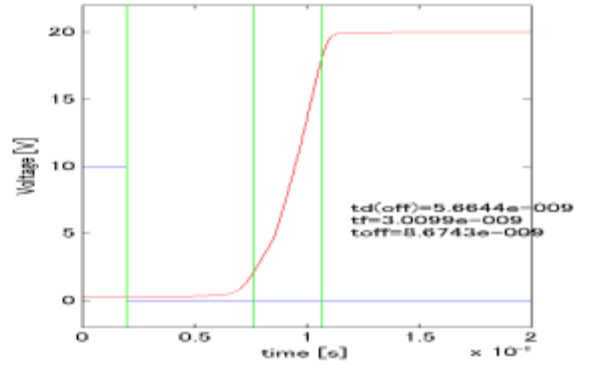
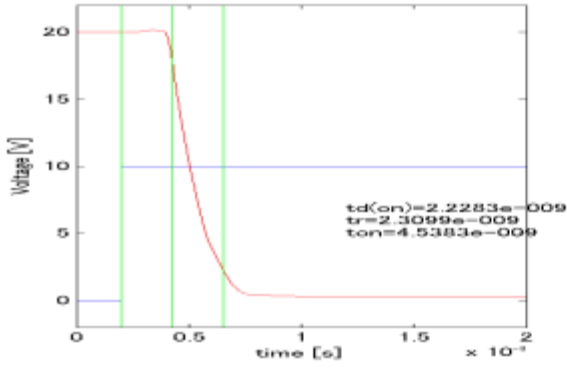
vdd = 20V, didt = 100A/us, Temp = 25degC



Simulation results are following.  
 Explanatory notes — : simulated

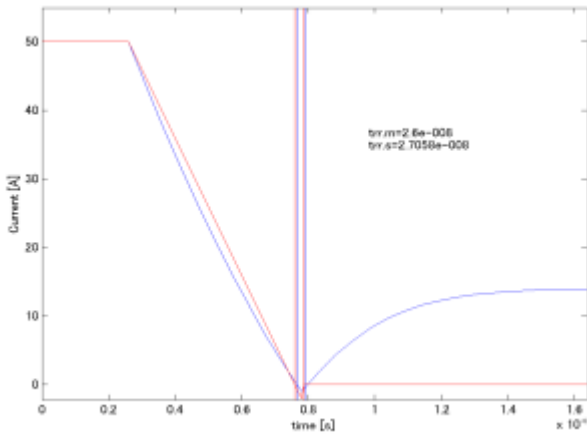
**Switching Waveform ( Blue : INPUT Red : OUTPUT )**

v<sub>gg</sub> = 10V, v<sub>dd</sub> = 20V, R<sub>GG</sub> = 3.5ohm, i<sub>dd</sub> = 80A



**Trr Waveform ( Red : Datasheet Blue : Simulation )**

v<sub>dd</sub> = 20V, didt = 100A/us, Temp = 25degC, i<sub>dd</sub> = 50A



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