

MDC_BD244C_LT

LTspice Model PNP ON BD244C

Model Information

ModelMextram modelCall NameMDC_BD244C_LTPin Assign1:C 2:B 3:EFile ListModel Library
Model Report

MDC_BD244C_LT01.lib MDC_BD244C_LT.pdf (this file)

Verified Simulator Version Note

LTspice version XVII

References

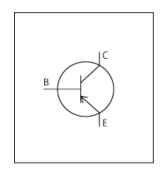
The information which was used for modeling is as follow:

Rev.A, February 2000 BD244C ON Semiconductor. hFElc[Temp],Vce(sat)lc[Temp],Vbe(sat)lc[Temp]

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Collector-emitter voltage (DC)	0	to	-100	V
Base current (DC)	0	to	-2	А
Temperature	-65	to	150	deg C

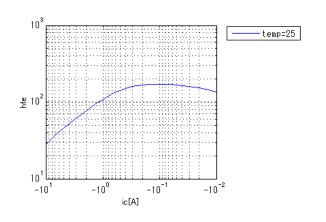




Simulation results are following. Explanatory notes -: simulated

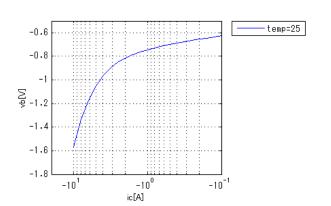
hFEIc[Temp]

Vce = -2V



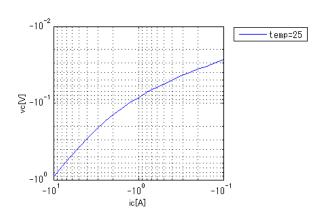
Vbe(sat)Ic[Temp]

IC/IB = 10.1



Vce(sat)lc[Temp]

IC/IB = 10.1





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