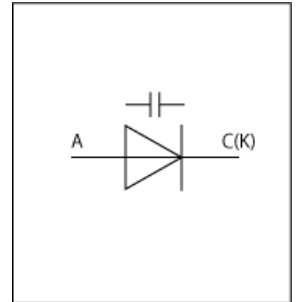


PSpice Model

Variable Capacitance Diode

TOSHIBA

JDV2S41FS



Model Information

Model A macro model based on general SPICE diode model
Call Name MDC_JDV2S41FS_PS
Pin Assign 1:A 2:C
File List Model Library MDC_JDV2S41FS_PS01.lib
 Model Report MDC_JDV2S41FS_PS.pdf (this file)

Verified Simulator Version PSpice version 16.6
Note

References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2014-03-01
- Product name JDV2S41FS
- Company name Toshiba Corporation
- Characteristics IrVr[Temp],CjVr

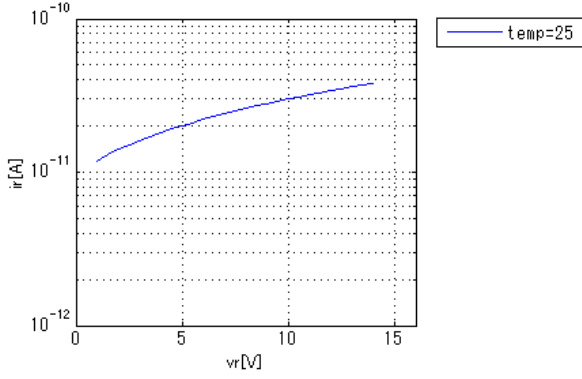
Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	30	V
Temperature	-55	to	125	deg C

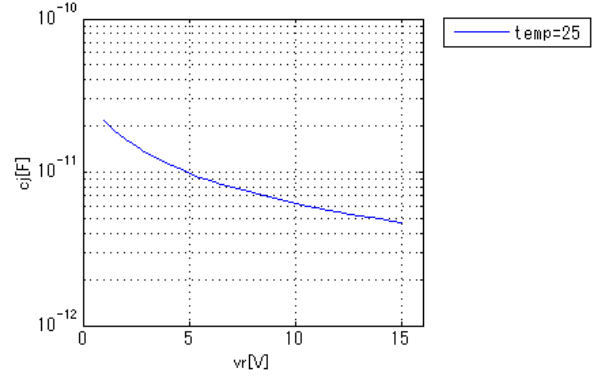
Simulation results are following.
 Explanatory notes — : simulated

IrVr[Temp]



CjVr

Freq. = 1MHz



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MoDeCH Inc.

Head Office

Location: Mitsuisemei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:<http://www.modech.com/en/>