

MDC_FMH06N90E_LT

LTspice Model NMOS FUJI ELECTRIC FMH06N90E

Model Information

 Model
 A macro model based on BSIM3 model
 1
 2
 3
 1
 3

 Call Name
 MDC_FMH06N90E_LT
 1:G 2:D 3:S
 1:G 2:D 3:S
 1:G 2:D 3:S
 MDC_FMH06N90E_LT01.lib

 File List
 Model Library
 MDC_FMH06N90E_LT01.lib
 MDC_FMH06N90E_LT01.lib

Verified Simulator Version Note

LTspice version XVII

References

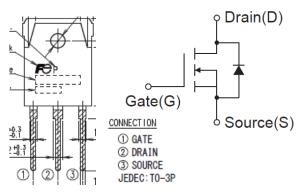
The information which was used for modeling is as follow:

Unknown FMH06N90E FUJI ELECTRIC CO., LTD. IdVds[Vgs],IdVgs[Temp],YfsId[Temp],Rds(on)Id[Vgs],Rds(on)Temp[Id],VthTemp[Id],VgsQg[Vdd],CapacitanceVds[Cname] ,IsVsd[Temp],SwitchingIdd[Tname],Trrlf[Ir],Qrrlf[Ir],Switching Waveform,TrrWaveform

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Drain-source voltage (DC)	0	to	900	V
Gate-source voltage (DC)	-30	to	30	V
Temperature	-55	to	150	deg C

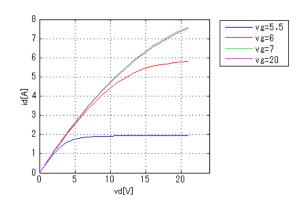




Simulation results are following. Explanatory notes — : simulated

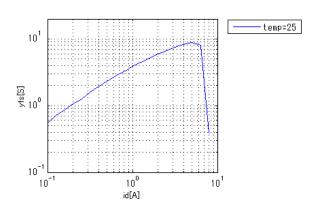
ldVds[Vgs]

Temp = 25degC



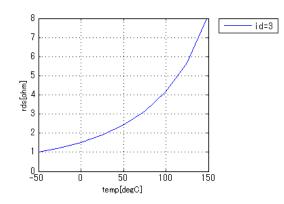
Yfsld[Temp]

Vds = 25V



Rds(on)Temp[Id]

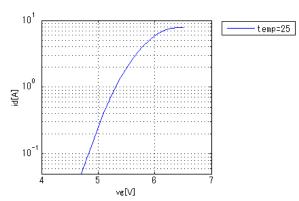
Vgs = 10V



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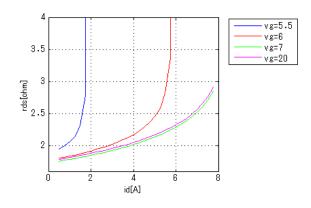
ldVgs[Temp]

Vds = 25V



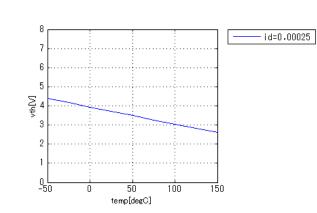
Rds(on)Id[Vgs]

Temp = 25degC



VthTemp[Id]

Vd = Vg

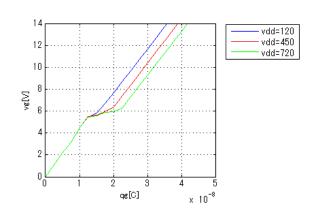




Simulation results are following. Explanatory notes — : simulated

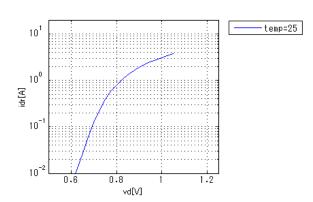
VgsQg[Vdd]

Id = 6A



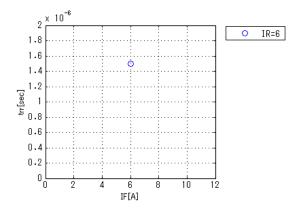
IsVsd[Temp]

vg = 0V



Trrlf[lr]

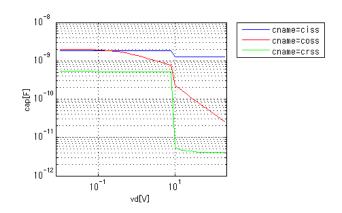
vdd = 25V, didt = 100A/us, Temp = 25degC



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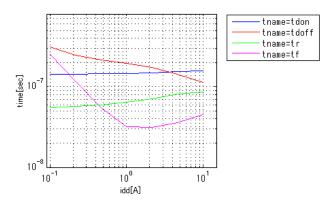
CapacitanceVds[Cname]

freq = 1000000Hz



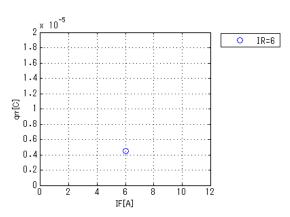
SwitchingIdd[Tname]

vgg = 10V, vdd = 600V, RGG = 390hm



Qrrlf[lr]

vdd = 25V, didt = 100A/us, Temp = 25degC

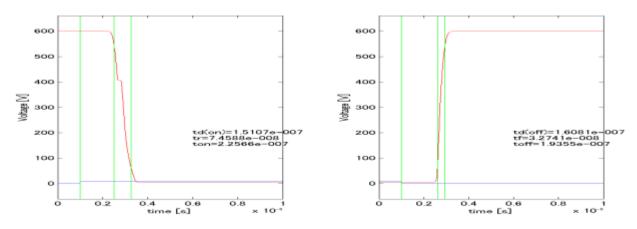




Simulation results are following. Explanatory notes — : simulated

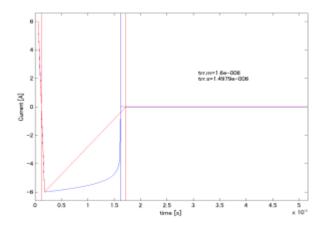
Switching Waveform (Blue : INPUT Red: OUTPUT)

vgg = 10V, vdd = 600V, RGG = 39ohm, idd = 3A



Trr Waveform (Blue : Datasheet Red : Simulation)

vdd = 25V, didt = 100A/us, Temp = 25degC, IF = 6A





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