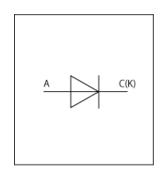


# PSpice Model ESD Protection Diode Panasonic DY2L5A0C0L



### **Model Information**

Model A macro model based on general SPICE diode model

Call Name MDC DY2L5A0C0L PS

Pin Assign 1:A 2:C

File List Model Library MDC\_DY2L5A0C0L\_PS02.lib

Model Report MDC\_DY2L5A0C0L\_PS.pdf (this file)

**Verified Simulator Version** 

Note

PSpice version 16.6

#### References

The information which was used for modeling is as follow:

[Data Sheet]

Date/Version Revision. 1Product name DY2L5A0C0L

Company name Panasonic Corporation

Characteristics IrVr[Temp],CjVr,IrVr(Breakdown)

#### Simulation Range

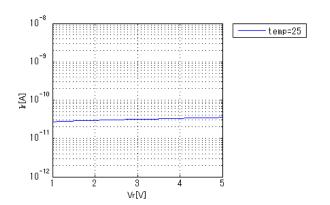
This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	15	V
Temperature	-55	to	150	deg C



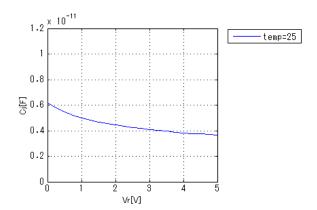
Simulation results are following. Explanatory notes — : simulated

## IrVr[Temp]

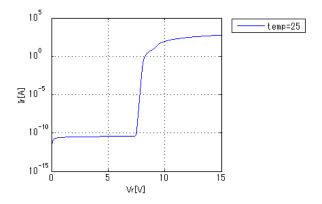


## CjVr

Freq. = 1MHz



### IrVr(Breakdown)





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MoDeCH Inc.

**Head Office** 

Location: Mitsuiseimei Hachioji Bldg., 5-15 Yokoyama-cho, Hachioji-Shi, Tokyo 192-0081, Japan

Tel:+81-42-656-3360

E-Mail:model-on-support@modech.co.jp

URL:http://www.modech.com/en/

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