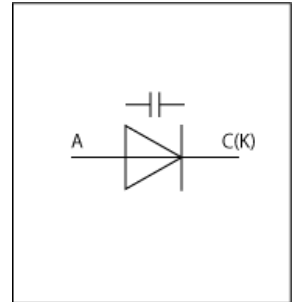


# PSpice Model

## Variable Capacitance Diode

### TOSHIBA

### JDV2S09FS



#### Model Information

**Model** A macro model based on general SPICE diode model  
**Call Name** MDC\_JDV2S09FS\_PS  
**Pin Assign** 1:A 2:C  
**File List** Model Library MDC\_JDV2S09FS\_PS01.lib  
 Model Report MDC\_JDV2S09FS\_PS.pdf (this file)

**Verified Simulator Version** PSpice version 16.6  
**Note**

#### References

The information which was used for modeling is as follow:

[Data Sheet]

- Date/Version 2014-03-01
- Product name JDV2S09FS
- Company name Toshiba Corporation
- Characteristics CjVr

#### Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

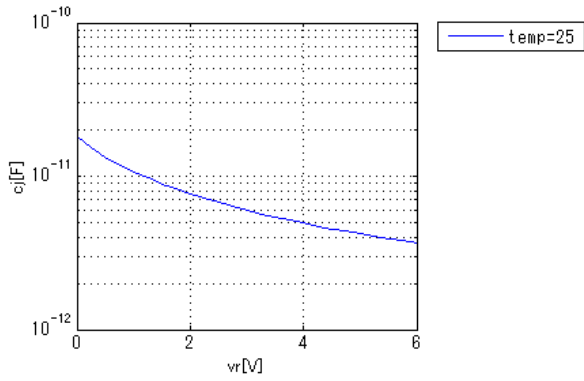
Item	Range			Unit
	Min.		Max.	
Reverse Voltage	0	to	20	V
Temperature	-55	to	150	deg C

Simulation results are following.

Explanatory notes — : simulated

### CjVr

Freq. = 1MHz



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