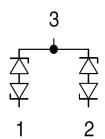


PSpice Model Zener Diode VISHAY INTERTECH VCAN26A2-03S



Model Information

Model A macro model based on general SPICE diode model

Call Name MDC_VCAN26A2-03S_PS

Pin Assign 1:C 2:C 3:C \times 2

File List Model Library MDC_VCAN26A2-03S_PS01.lib

Model Report MDC_VCAN26A2-03S_PS.pdf (this file)

Verified Simulator Version

Note

PSpice version 17.2

References

The information which was used for modeling is as follow:

[Data Sheet]

Date/VersionProduct nameRev. 1.4, 01-Feb-2022VCAN26A2-03S

Company name Vishay Intertechnology, Inc.

● Characteristics CjVr,VrIr[Temp],VrIr[Temp]2,IrVr[Temp],SurgeReverseCurre

ntWaveform(1A),SurgeReverseCurrentWaveform(3A)

Simulation Range

This table shows the range of evaluated simulation range that was not occurs any convergence problems in this area.

Item	Range			Unit
	Min.		Max.	
Zener Voltage	28(at 1mA)	to	32(at 1mA)	V
Clamping Voltage(max)	39(at 3A)	to	39(at 3A)	V
Temperature	-55	to	150	deg C



Model Functions Table

Diode

O: Implemented

×: Not Implemented

—: Not applicable

RANK=1

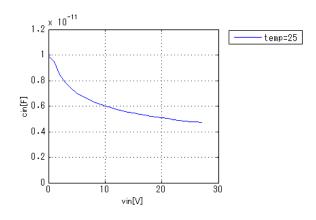
	10 (14)(2	
Functions	RANK	Implemented
IF-VF(Temp)	1	0
IR-VR(Temp)	1	0
Capacitance	1	0
Reverse recovery characteristics	1	_
Rectification characteristics(Bridge)	1	_



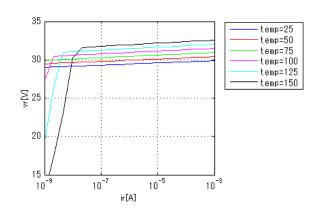
Simulation results are following. Explanatory notes — : simulated

CjVr

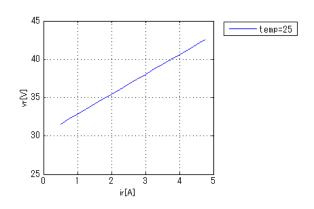
Freq = 1000000Hz



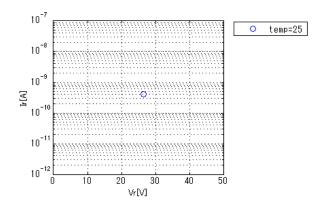
Vrlr[Temp] (Zener Voltage)



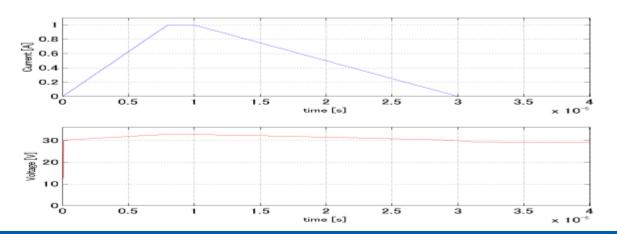
Vrlr[Temp]2 (Clamping Voltage)



IrVr[Temp] (Leak Current)



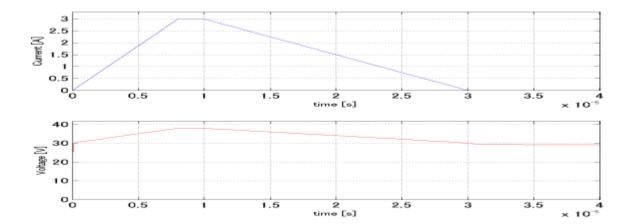
Surge Reverse Current Waveform (1A)





Simulation results are following. Explanatory notes — : simulated

Surge Reverse Current Waveform (3A)





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